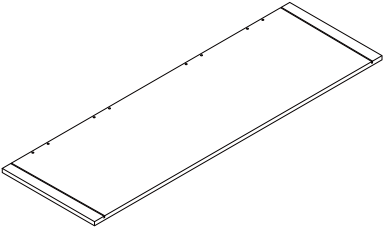
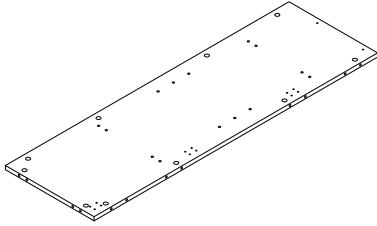
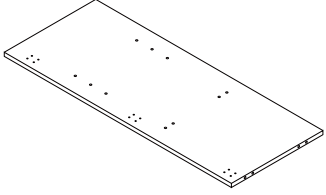
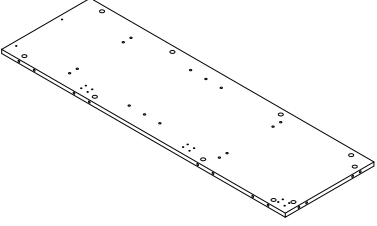
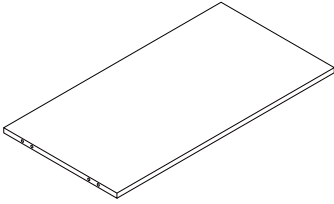
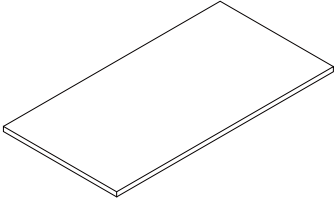
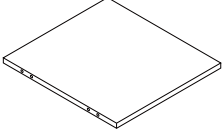
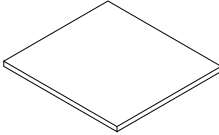
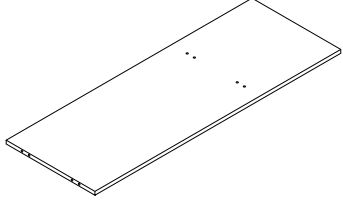
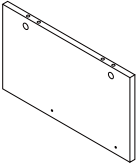
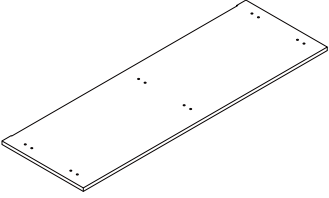
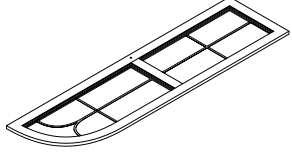
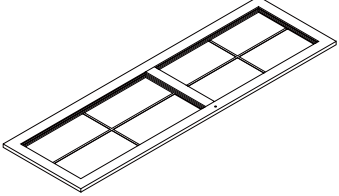
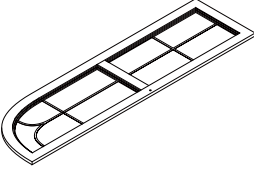

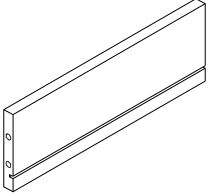
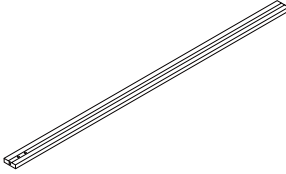
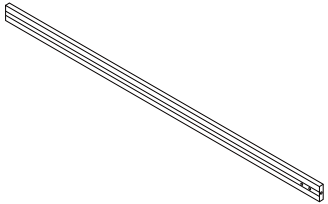

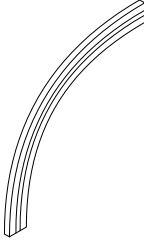
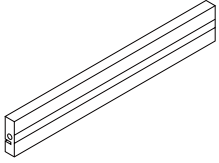
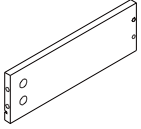
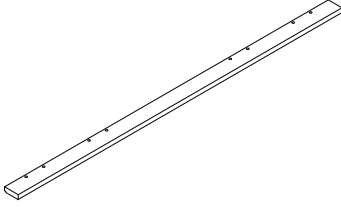
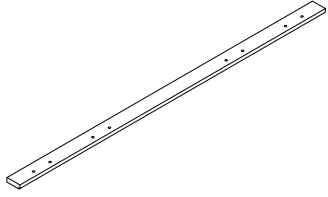
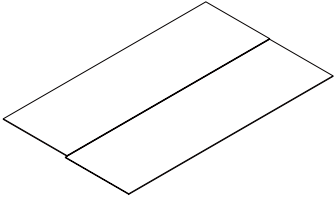
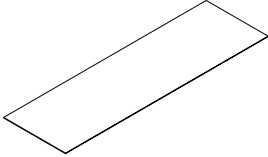

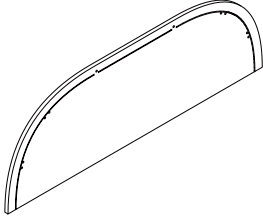
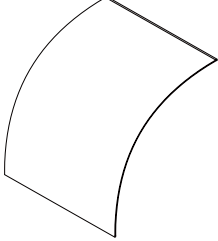
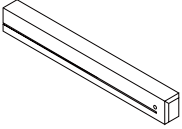
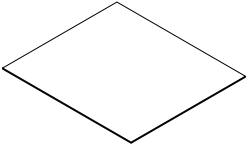
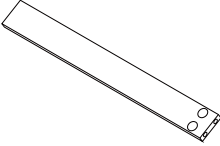
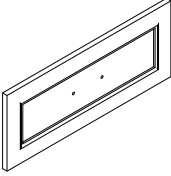
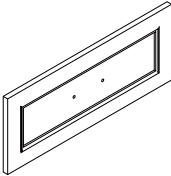
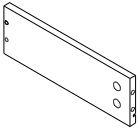
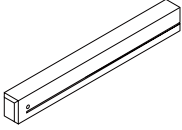
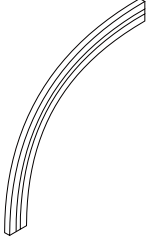


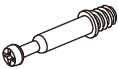

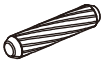
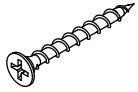
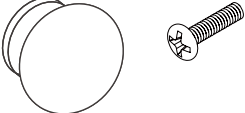


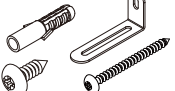
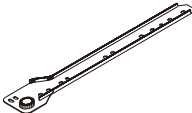
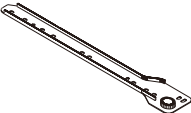
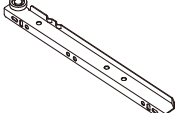
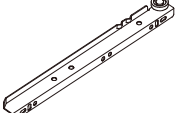

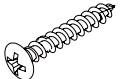
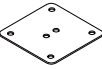



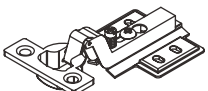

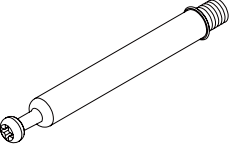


Parts and Operation Elements

		
① 1pc	② 1pc	③ 1pc
		
④ 1pc	⑤ 1pc	⑥ 1pc
		
⑦ 1pc	⑧ 1pc	⑨ 1pc
		
⑩ 1pc	⑪ 1pc	⑫ 1pc
		
⑬ 1pc	⑭ 1pc	⑮ 5pcs
		
⑯ 2pcs	⑰ 1pc	⑱ 1pc

Parts and Operation Elements

							
19	2pcs	20	1pc	21	1pc		
							
22	2pcs	23	1pc	24	1pc		
							
25	1pc	26	1pc	27	2pcs		
							
28	1pc	29	2pcs	30	1pc		
							
31	1pc	32	4pcs	33	1pc		
							
34	1pc	35	2pcs	36	1pc	37	1pc

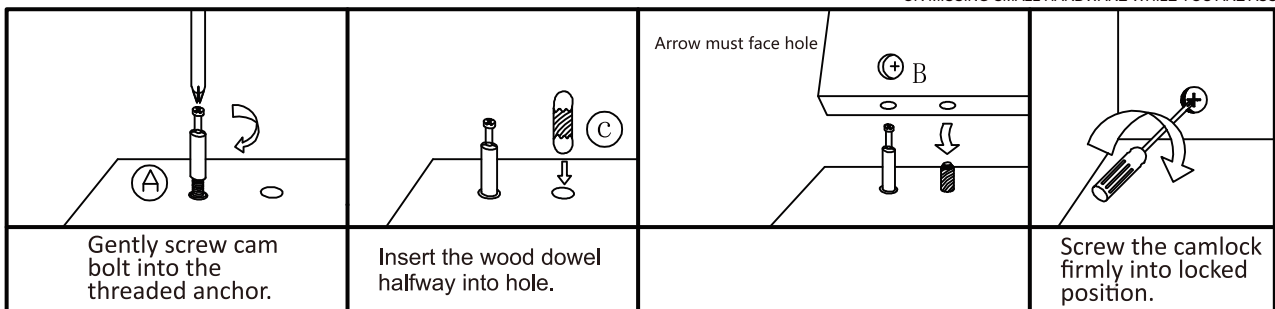
Hardware

			
A 56+2pcs	B 58+2pcs	C 50+2pcs	D 32 +2pcs
			
E 3pcs	F 16+2pcs	G 2pcs	H 2pcs
			
CL 2 pcs	CR 2 pcs	DL 2 pcs	DR 2 pcs
			
I 8+2pcs	J 4pcs	K 5pcs	L 20pcs
			
M. 8pcs	N 54+2pcs	O 9pcs	P. 27+2pcs
			
R 2pcs	S 2pcs	T 5pcs	



SPARE BAG

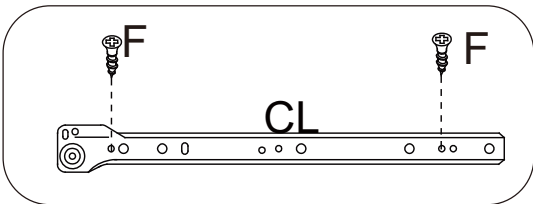
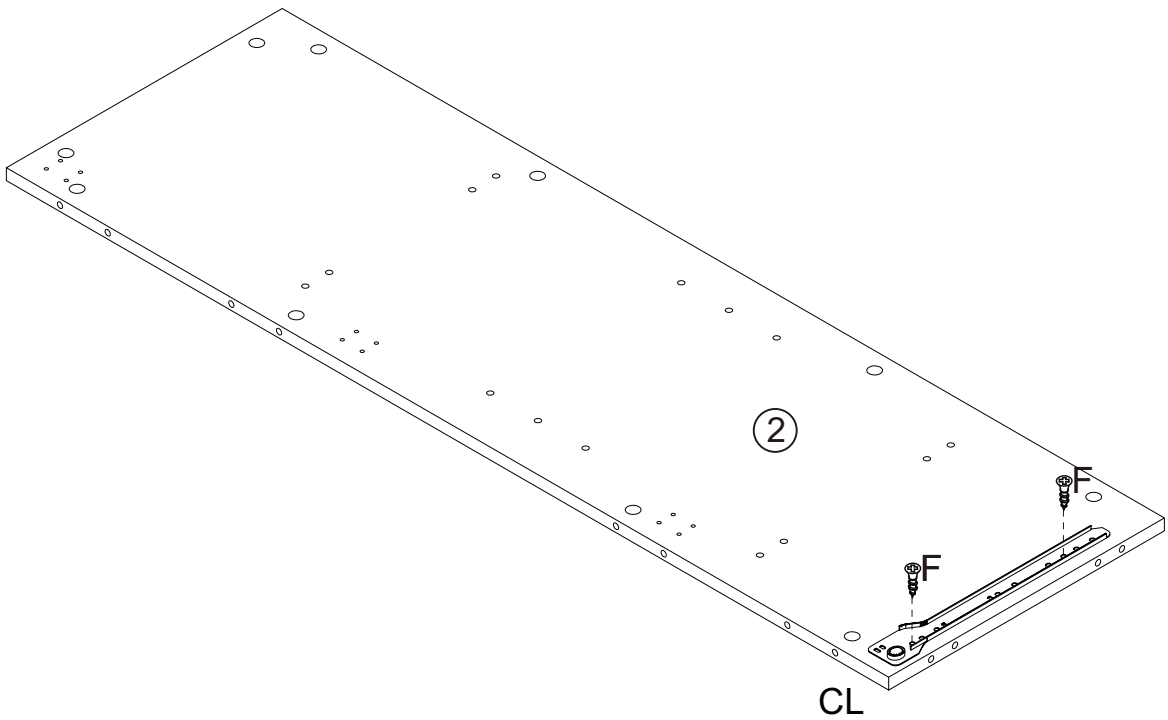
THE SPARE BAG INCLUDED EXTRA HARDWARE FOR DAMAGED OR MISSING SMALL HARDWARE WHILE YOU ARE ASSEMBLING



1

F x2

CL x1



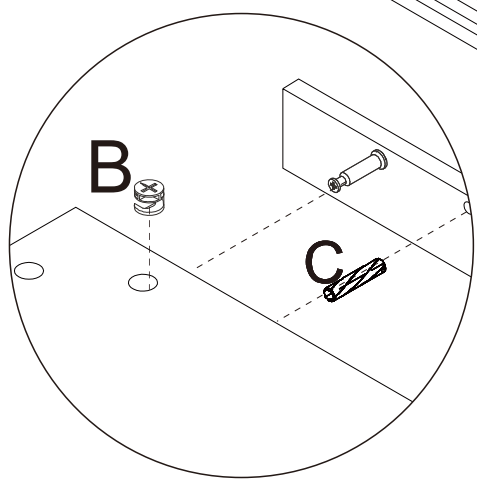
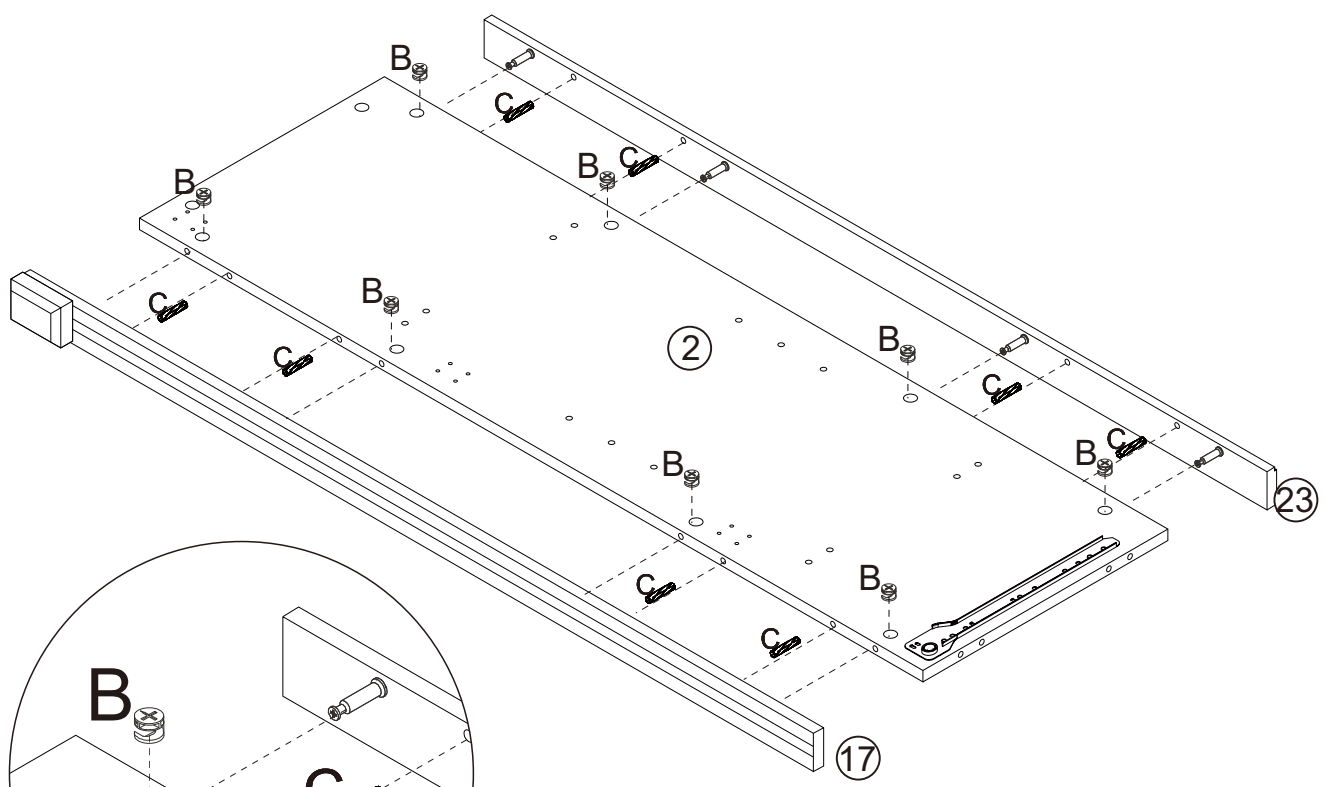
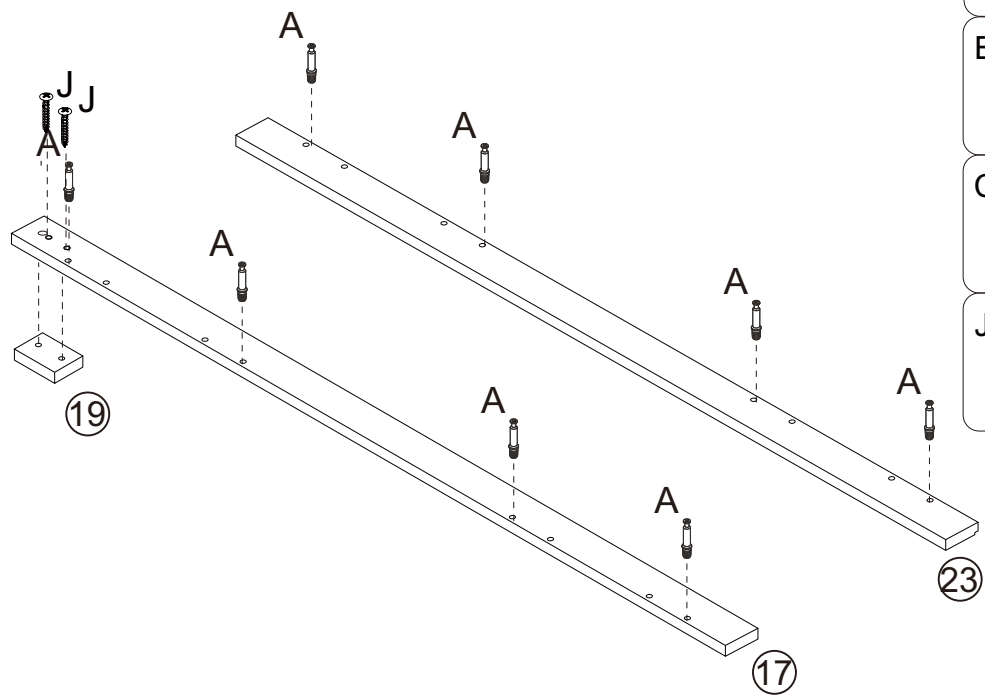
2

A x8

B x8

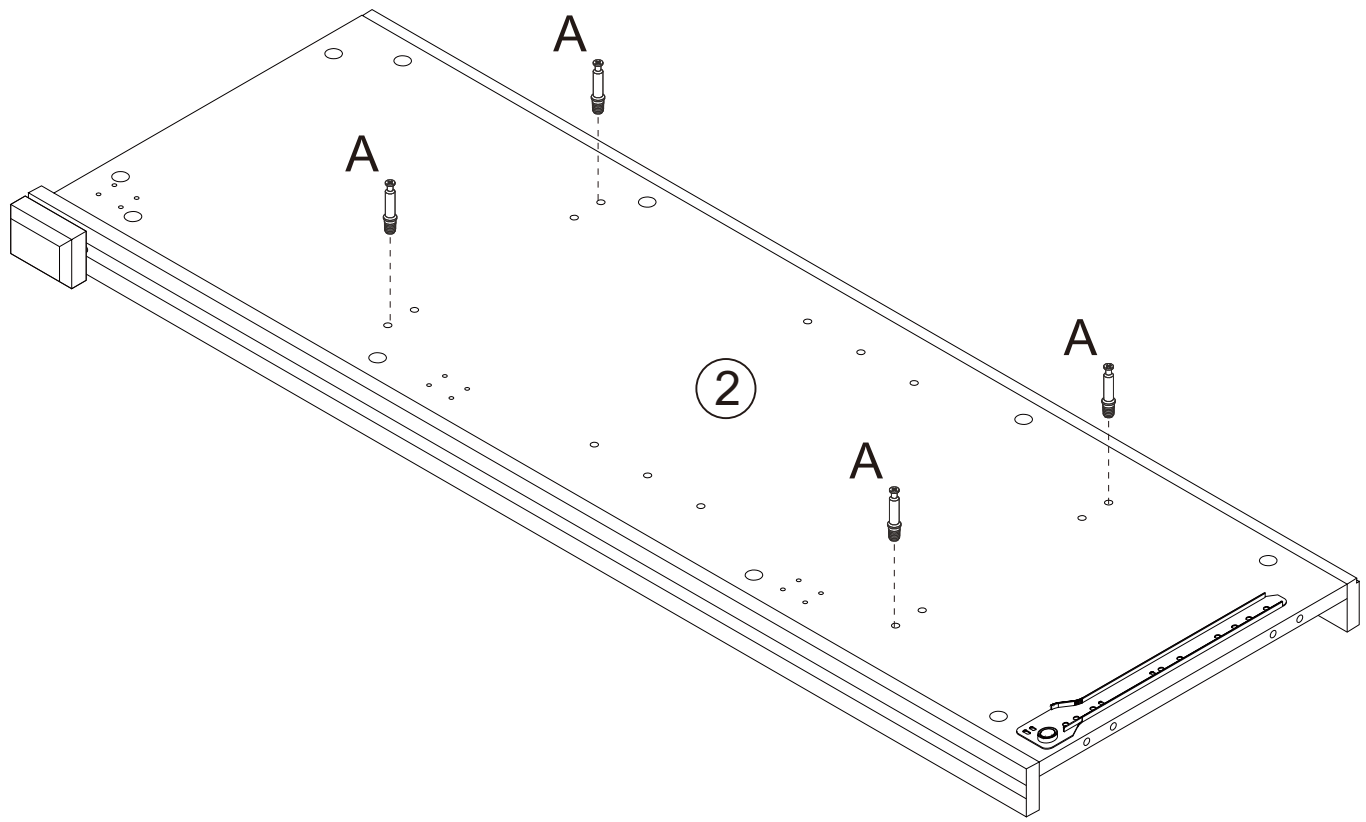
C x8

J x2



3

A x4

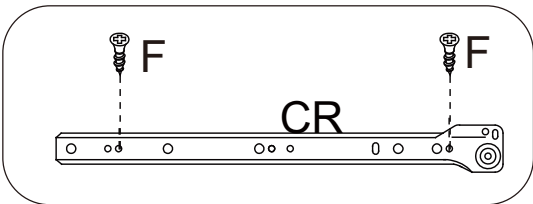
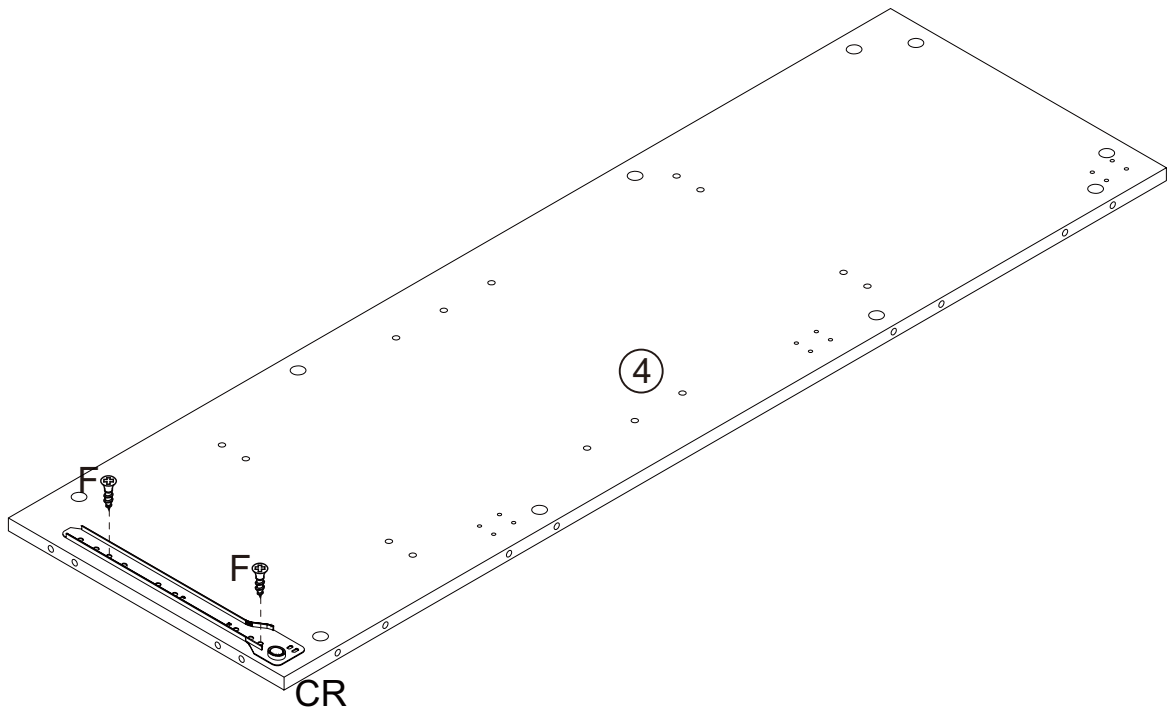


4

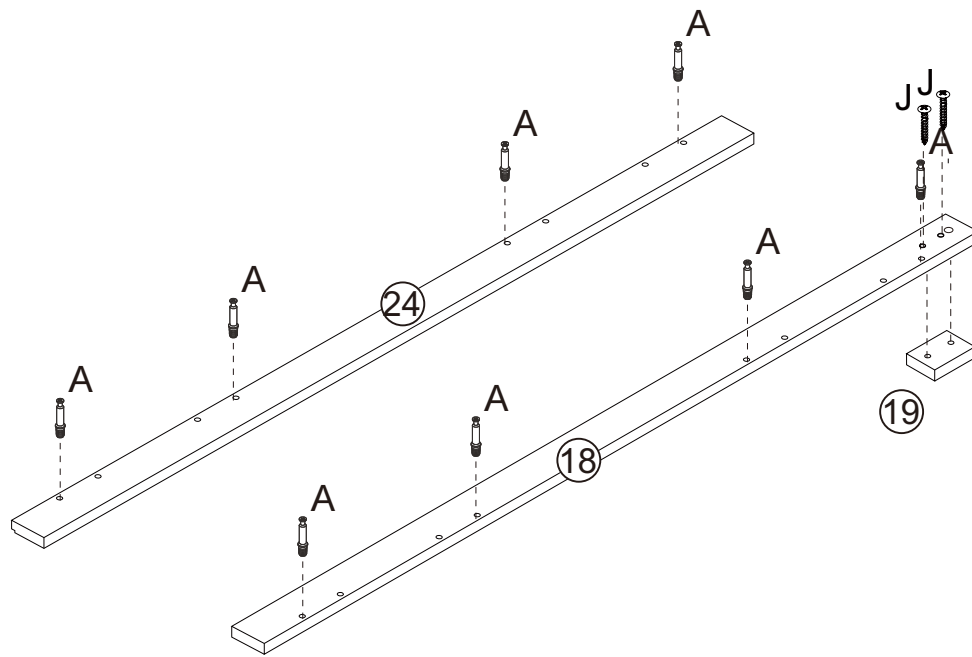
F x2







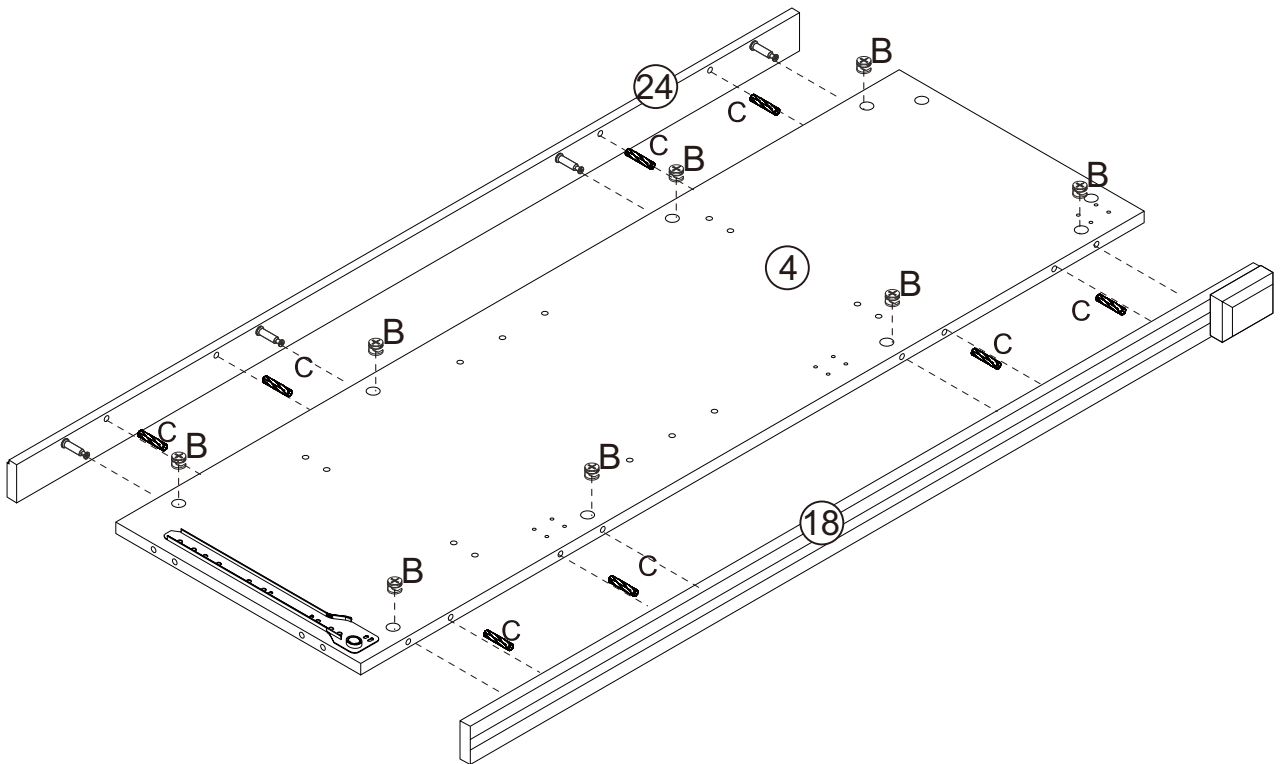
CR x1



5

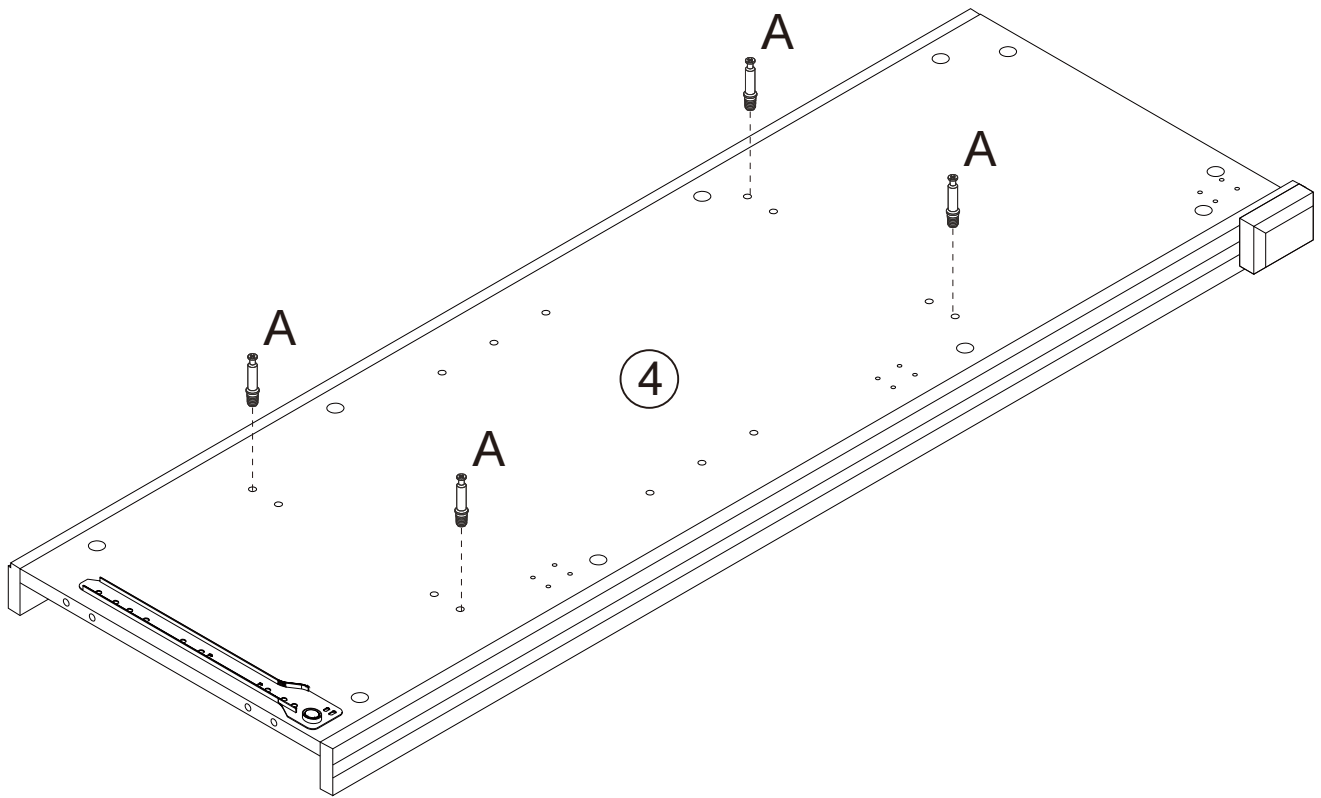


- A x8 
- B x8 
- C x8 
- J x2 



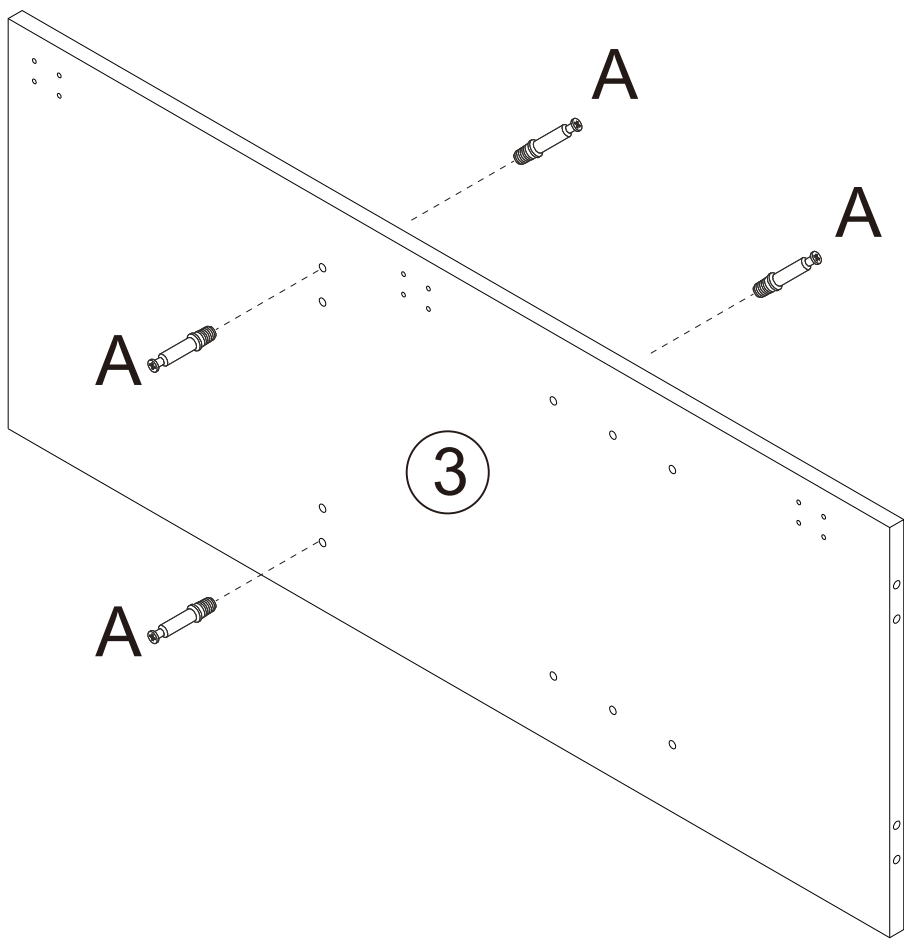
6

A x4



7

A x4

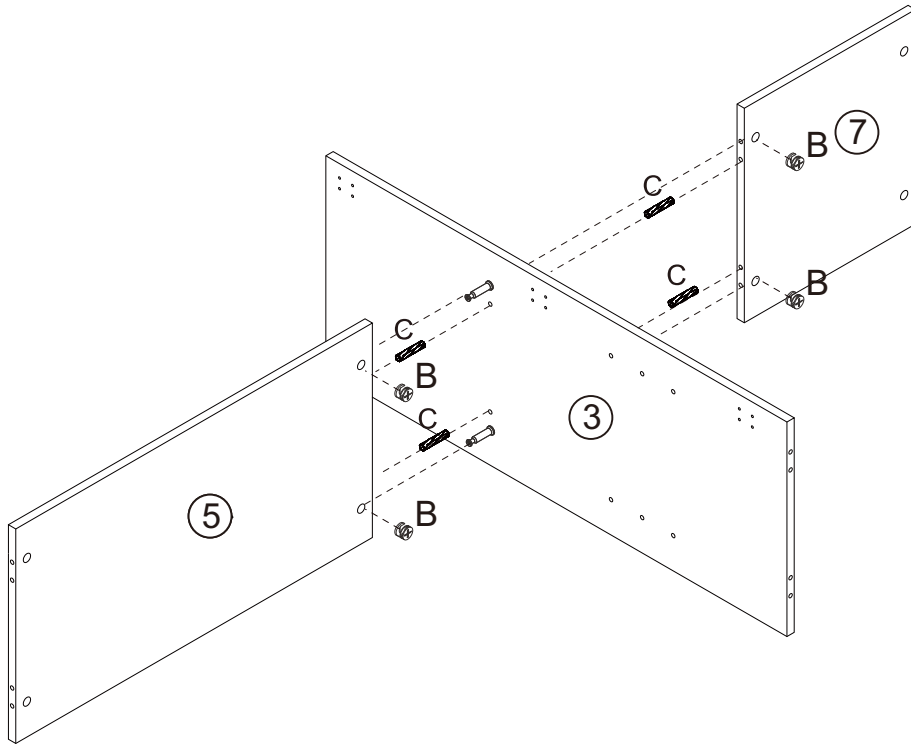


8

B x4



C x4



9

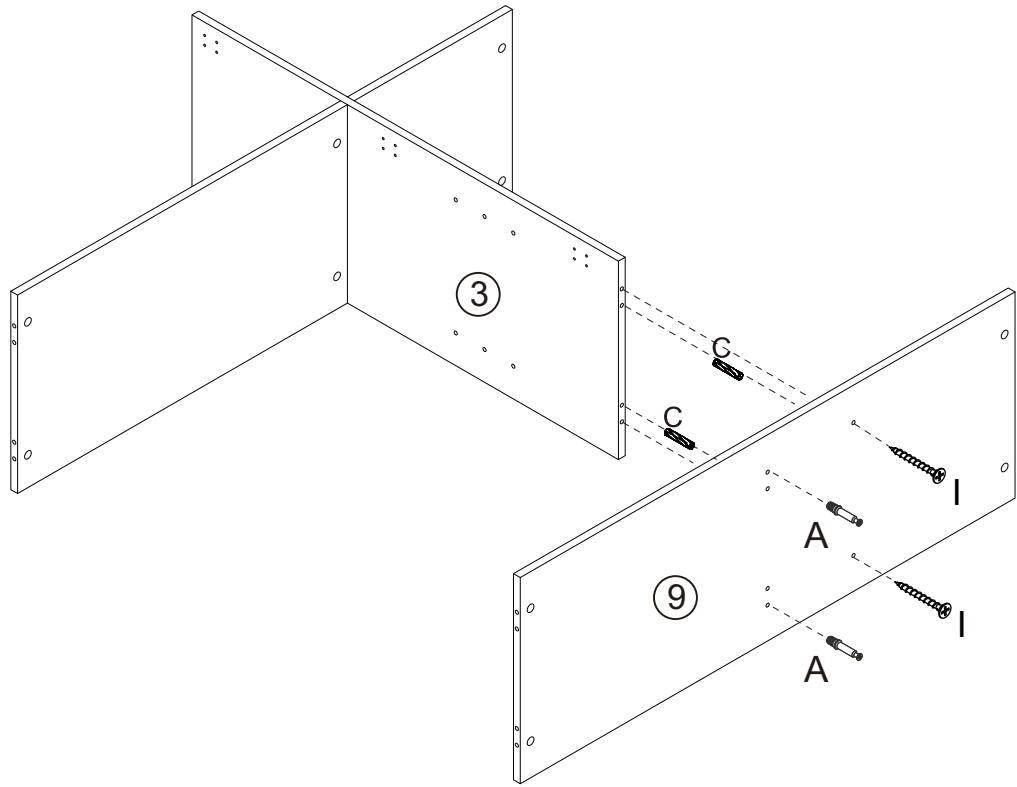
A x2



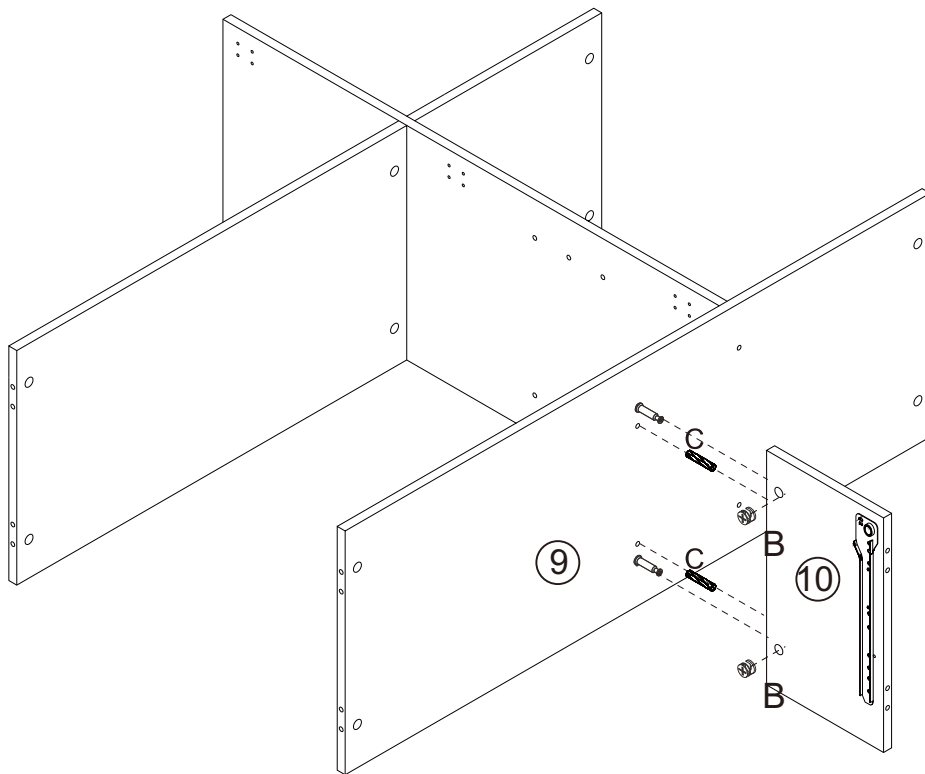
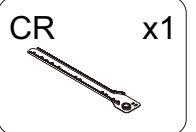
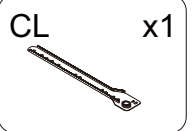
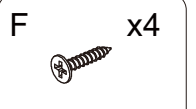
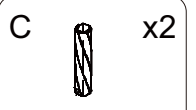
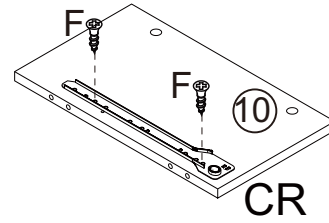
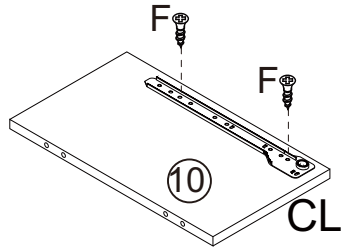
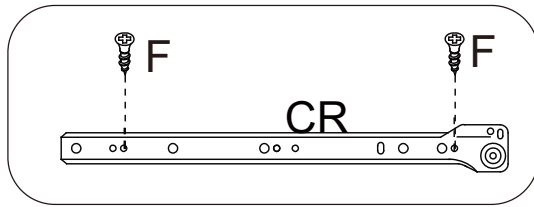
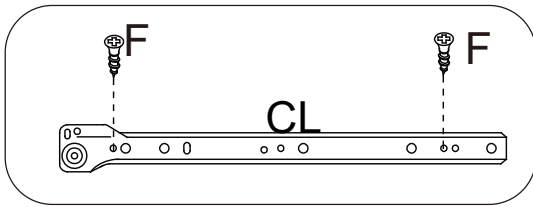
I x2



C x2



10

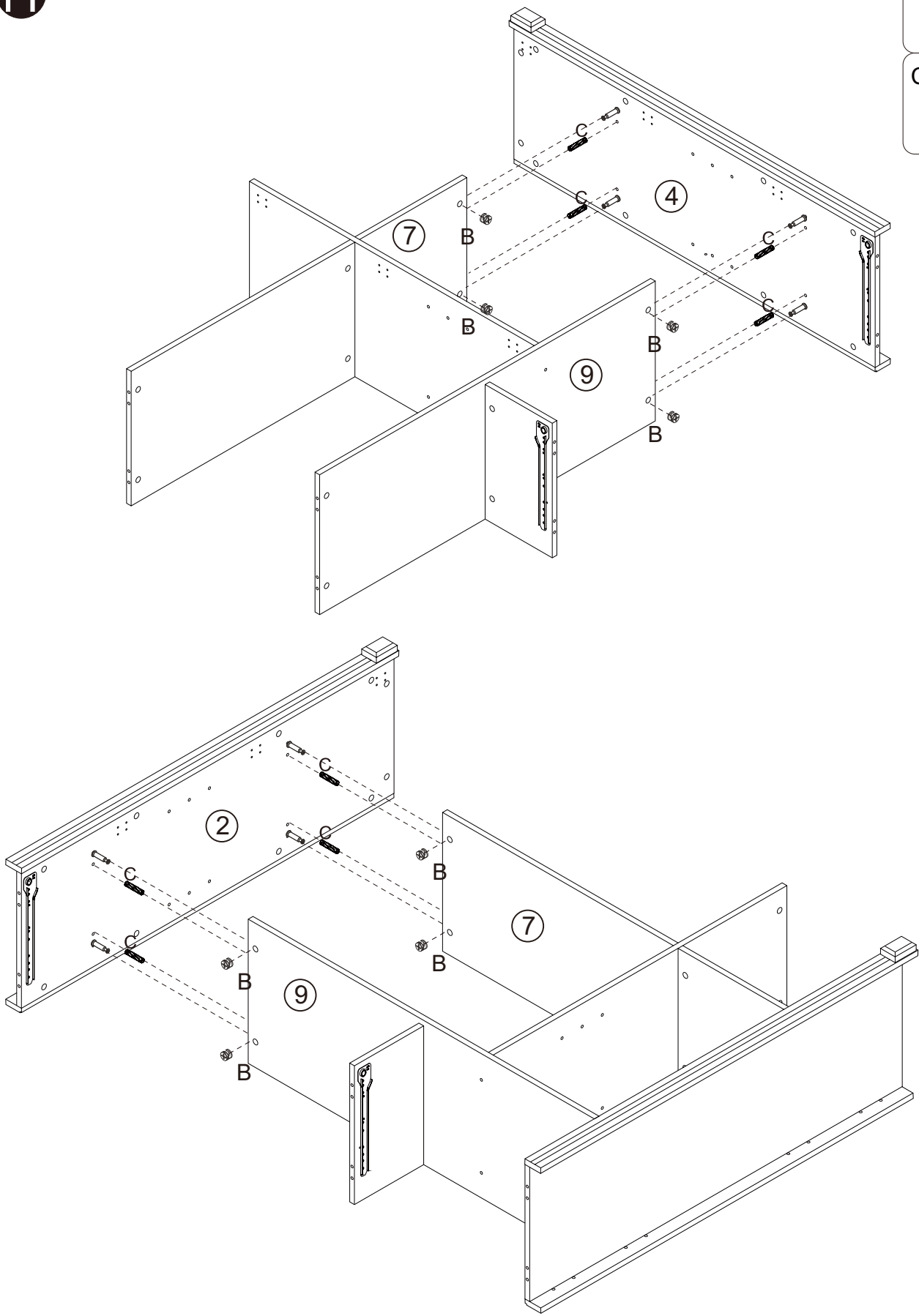


11

B x8

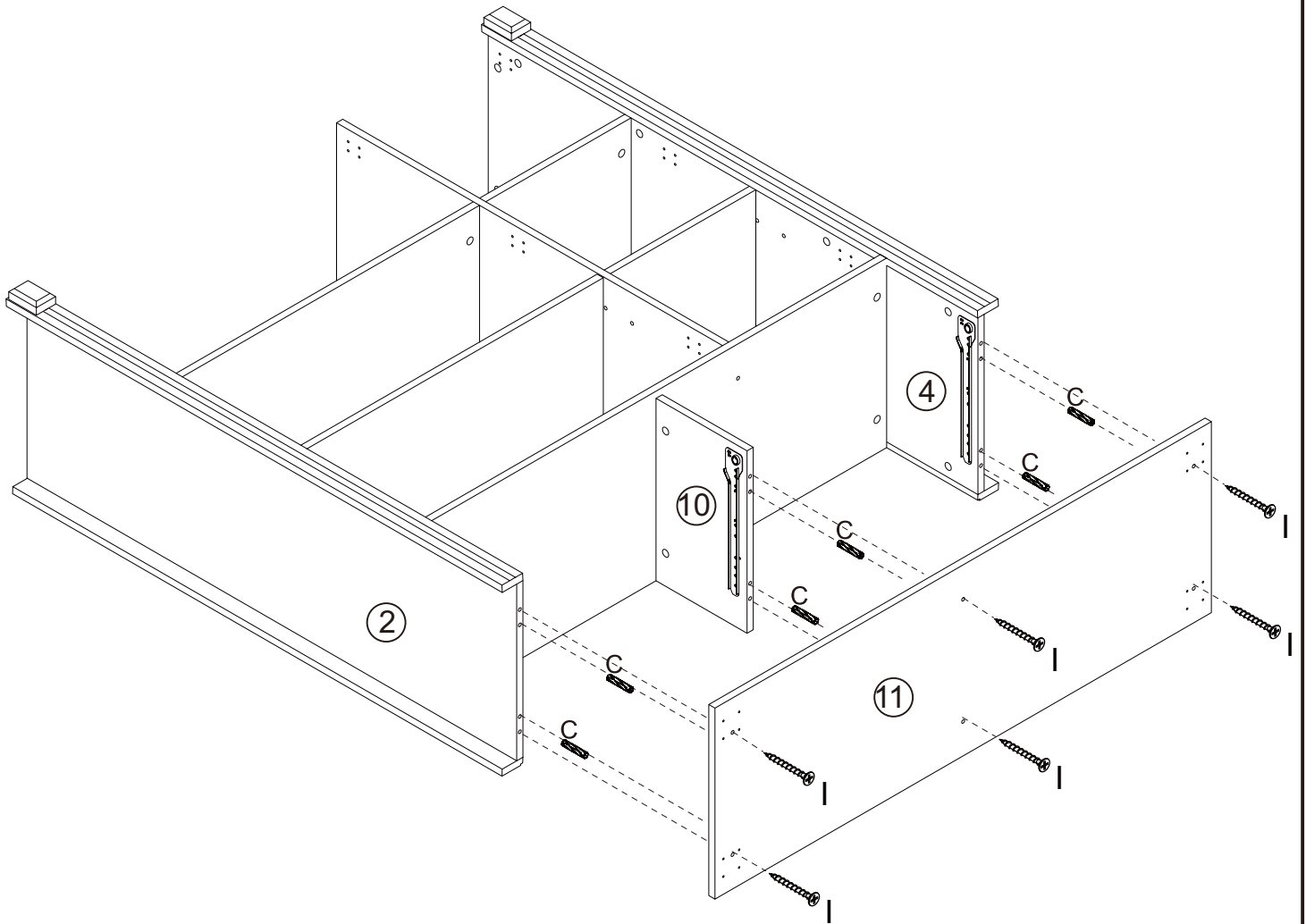


C x8



12

- I x6
- C x6

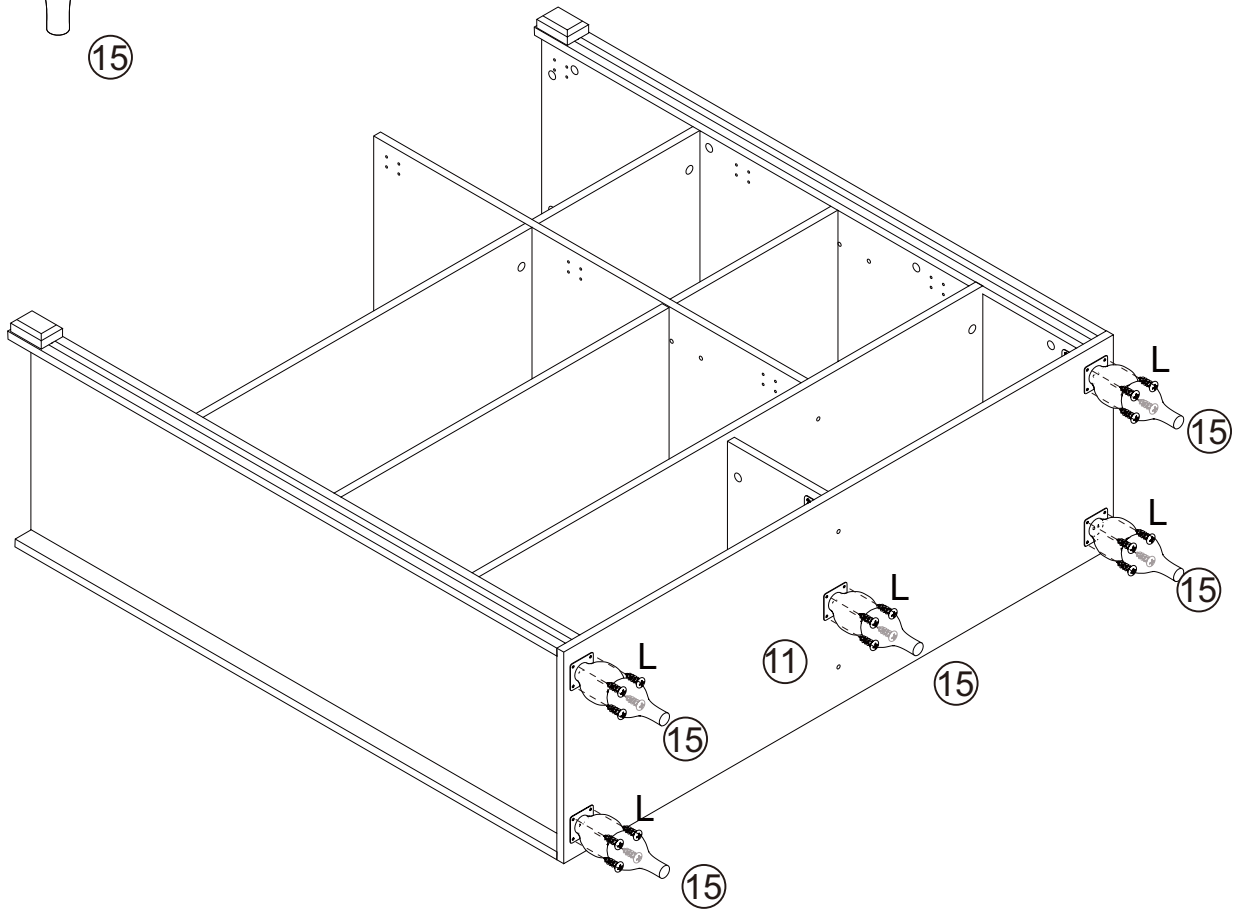
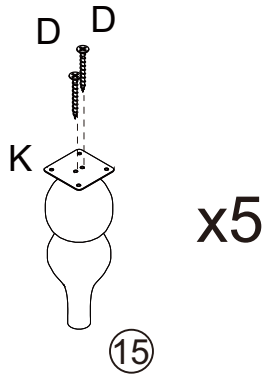


13




D x10

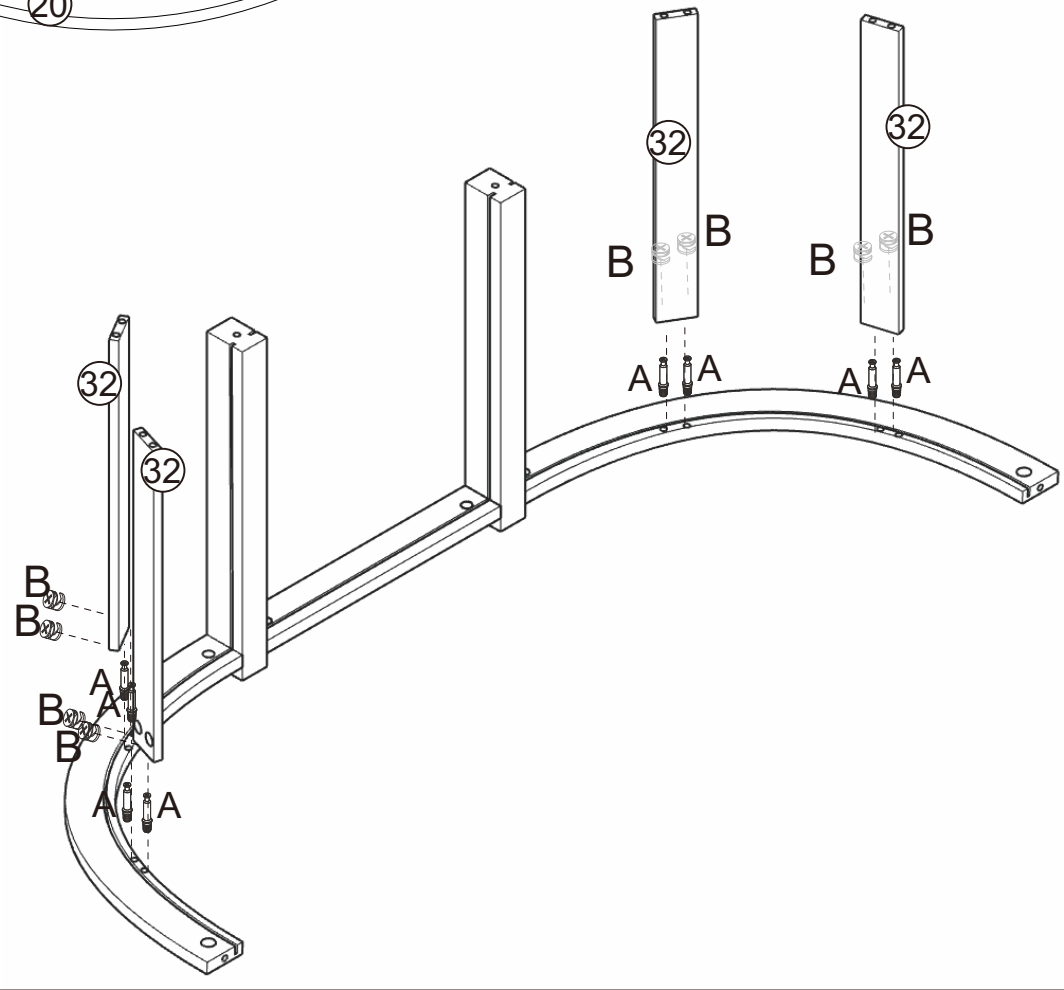
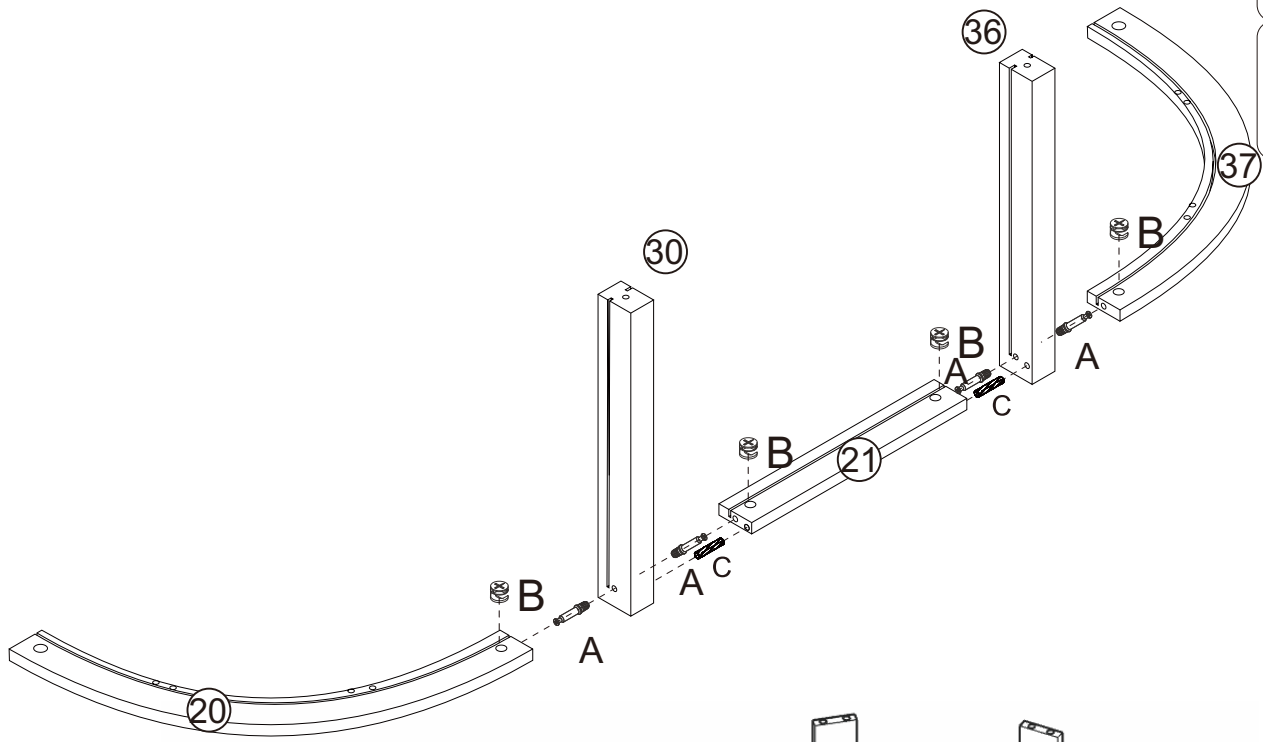
K x5

L x20

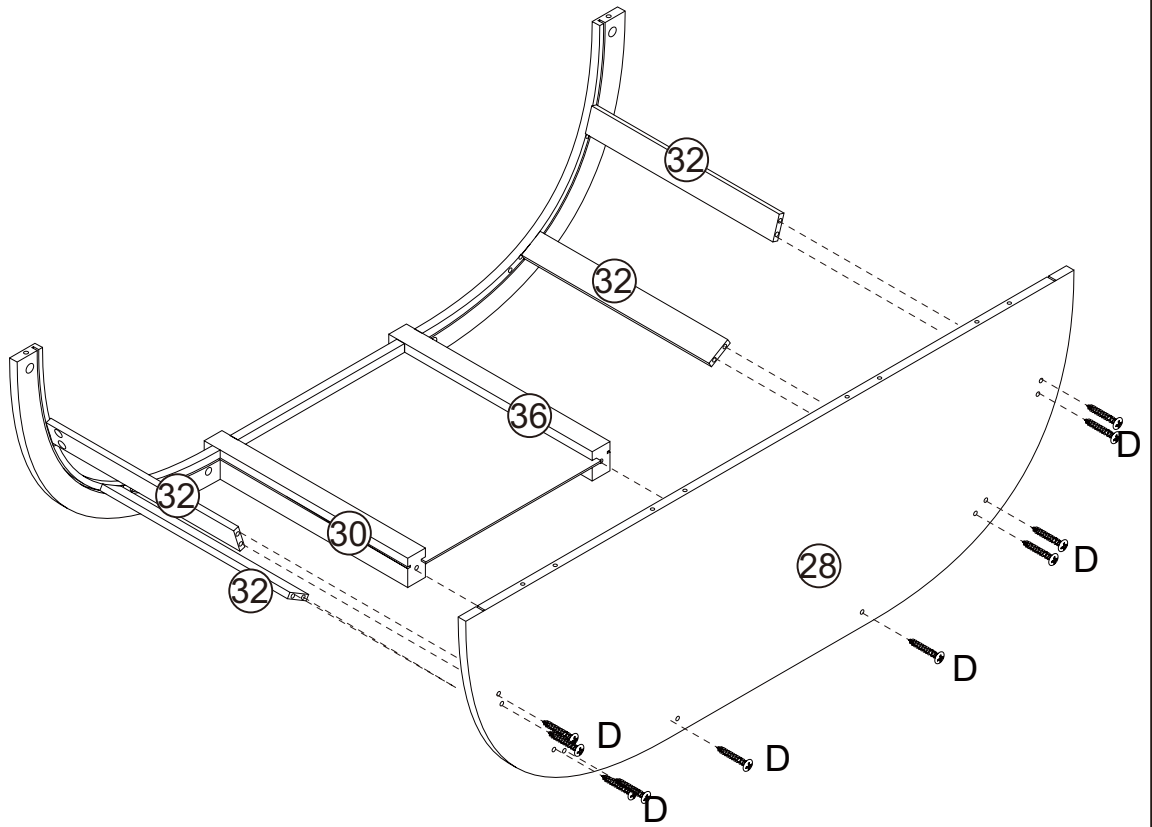
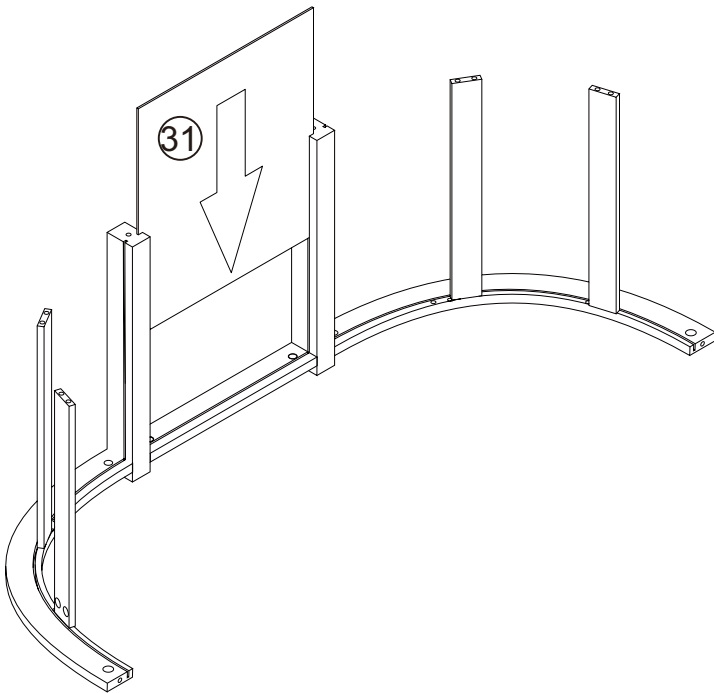
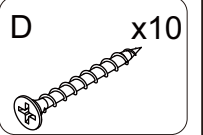


14

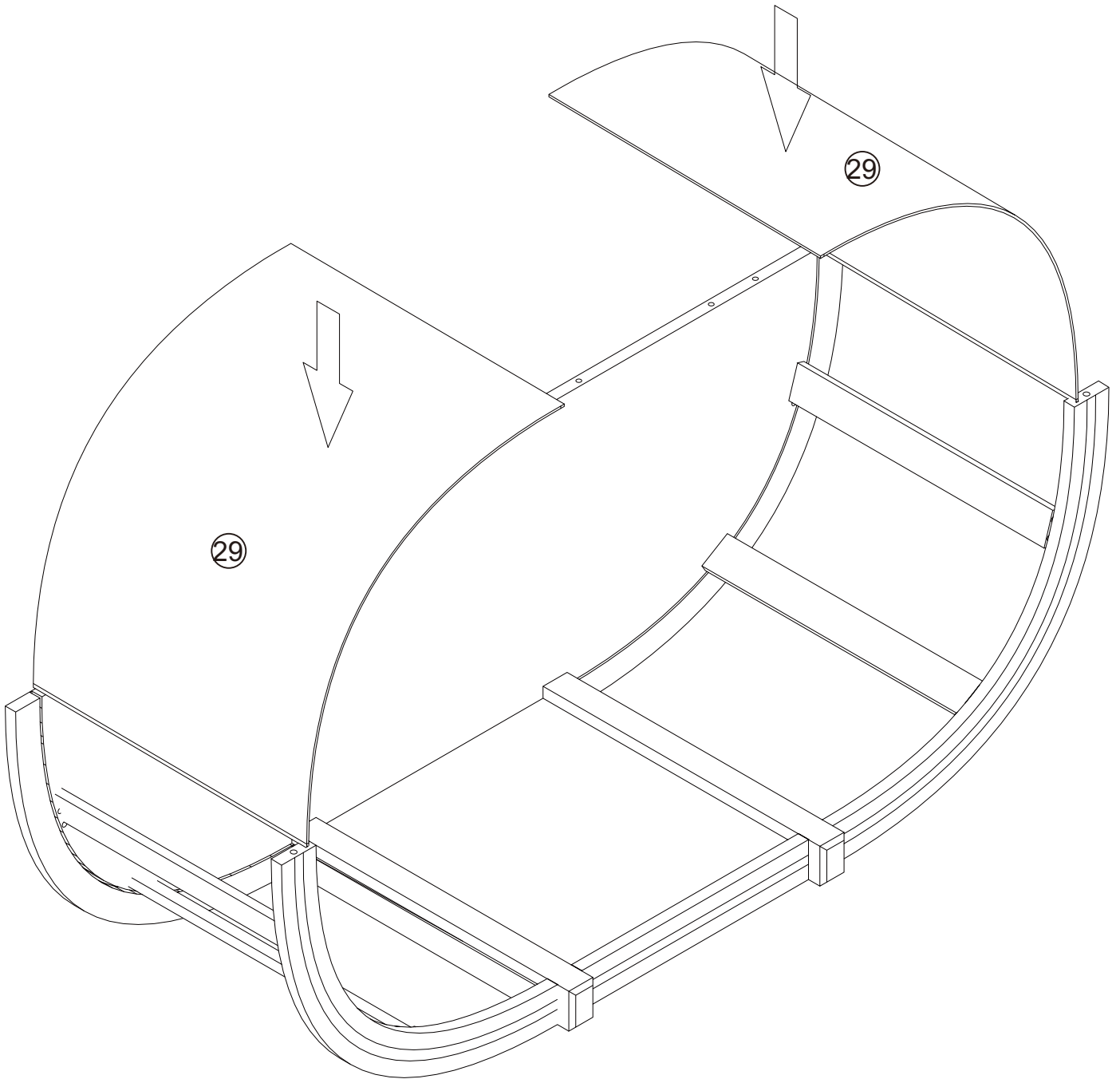
- A x12 
- B x12 
- C x2 



15



16

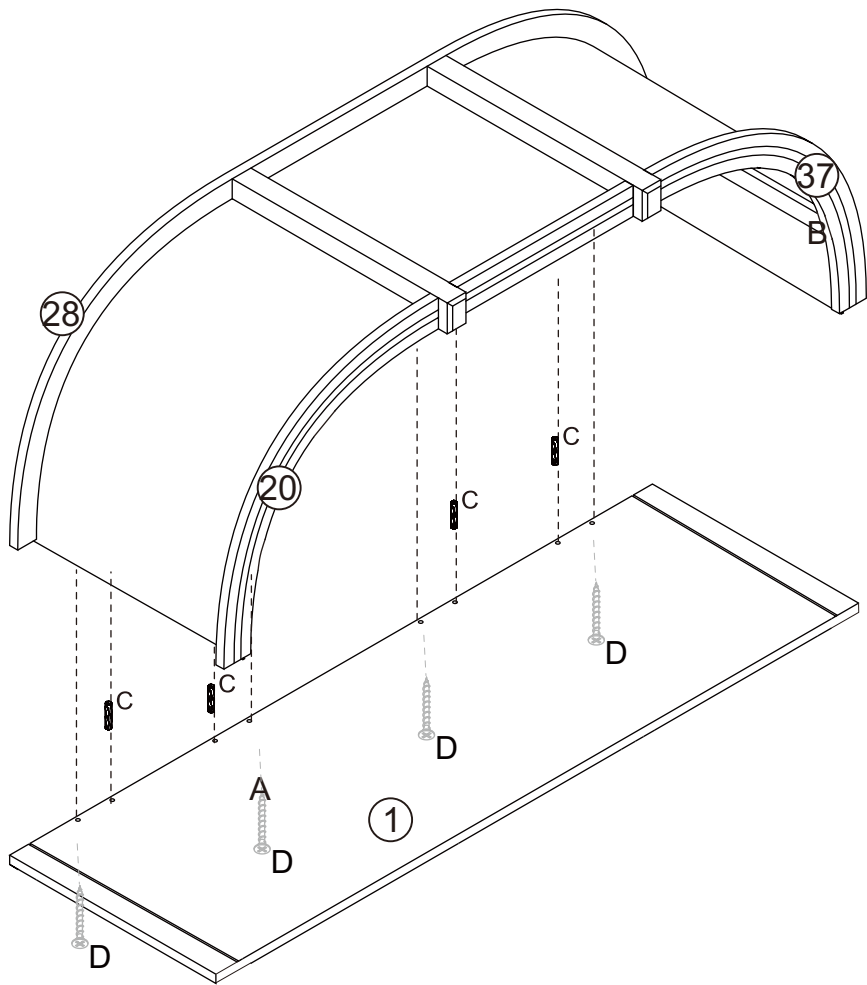
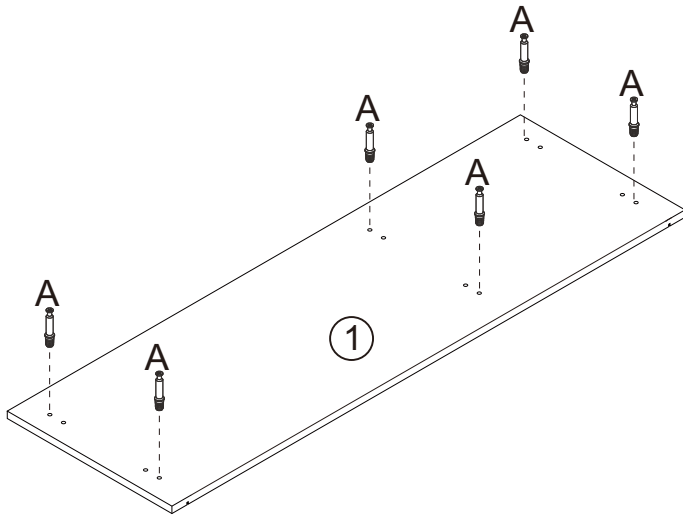


17

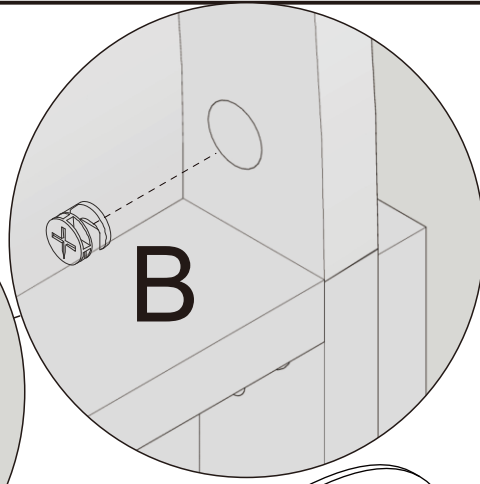
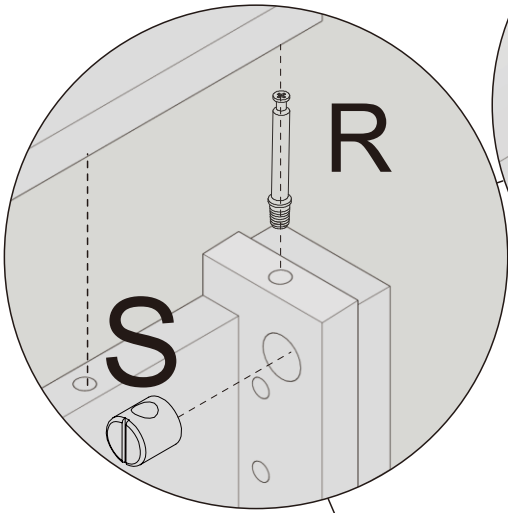
A x6





D x4

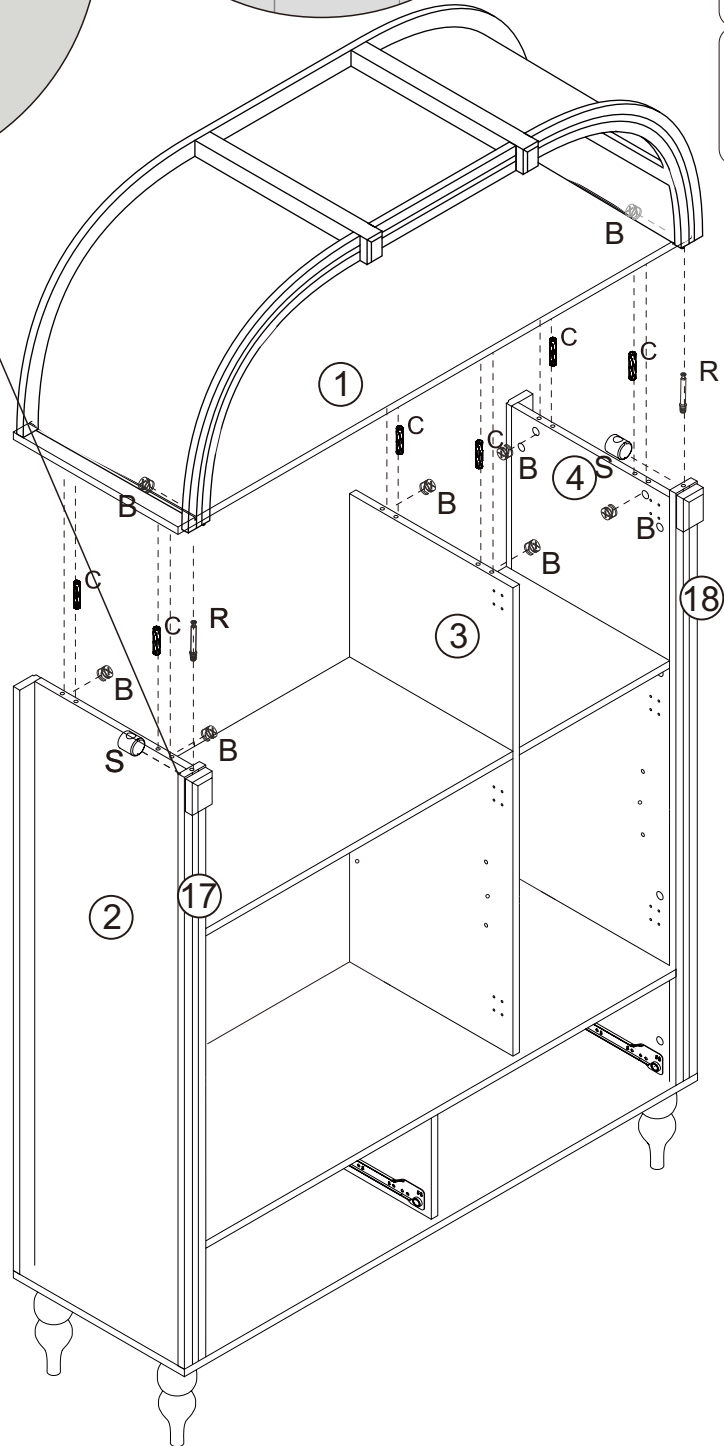
C x4



18



- R x2 
- B x8 
- C x6 
- S x2 

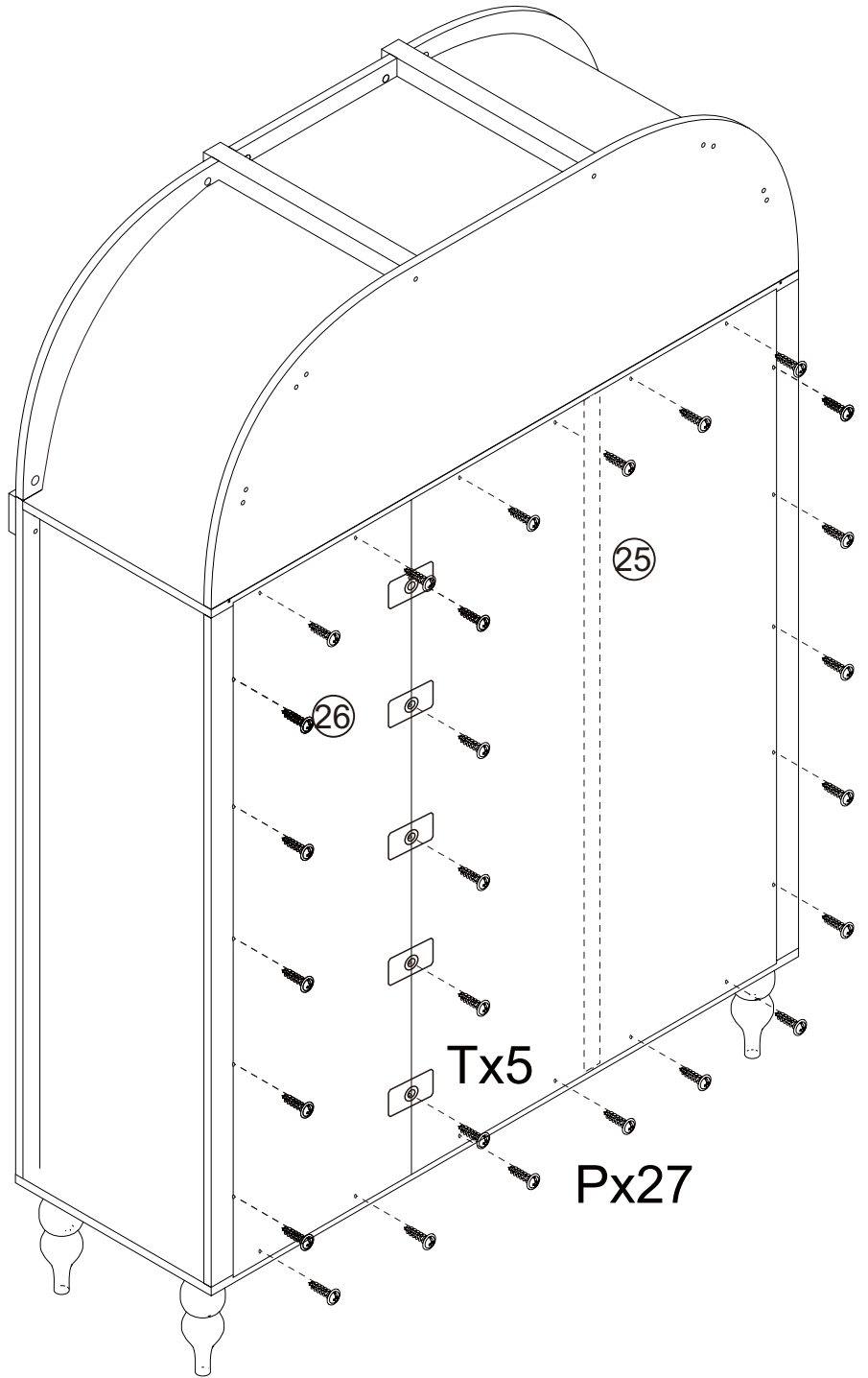
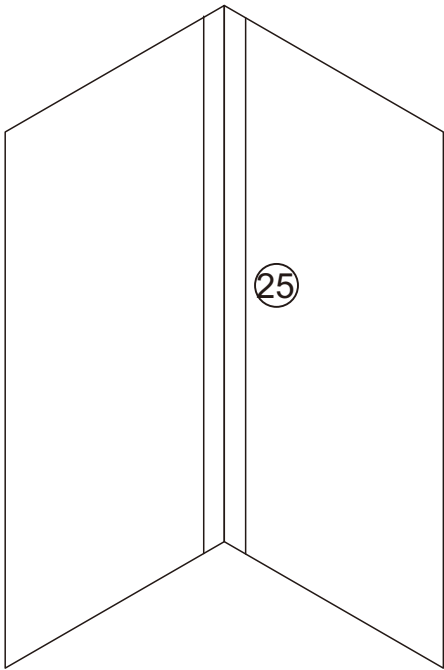
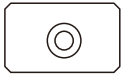


19

P. x27

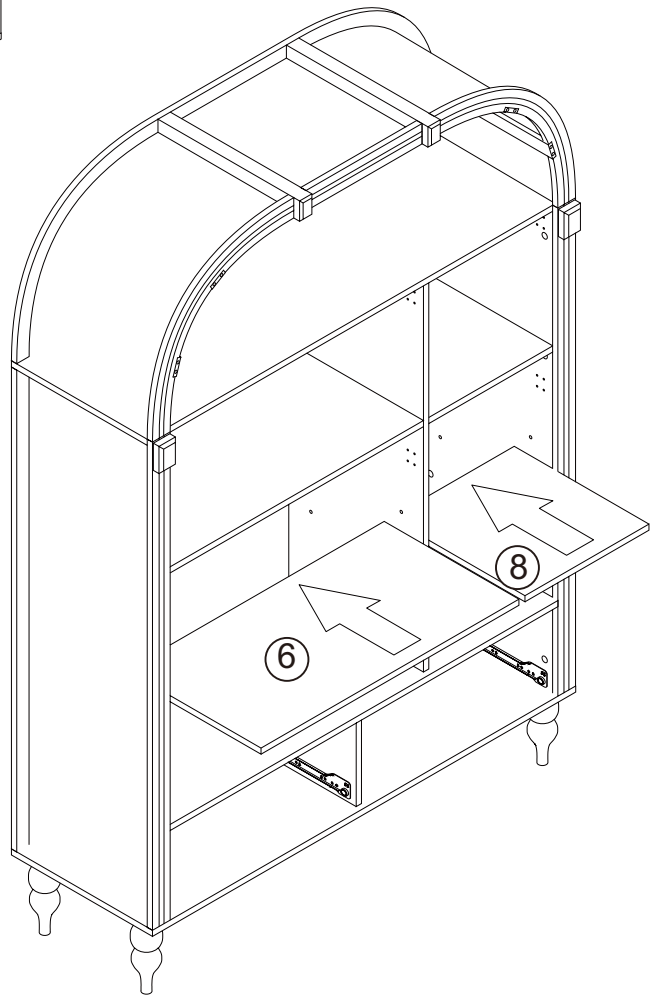
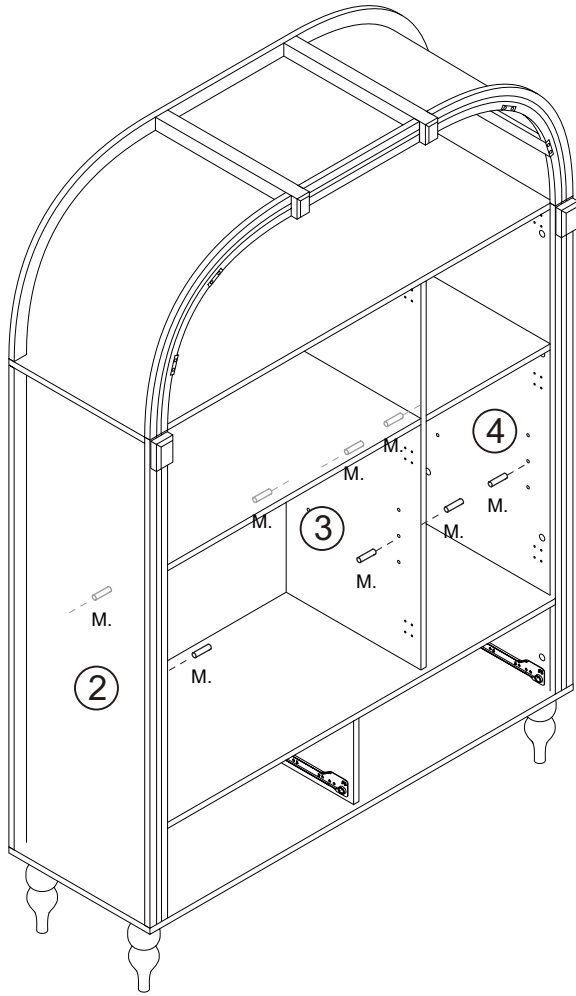


T x5

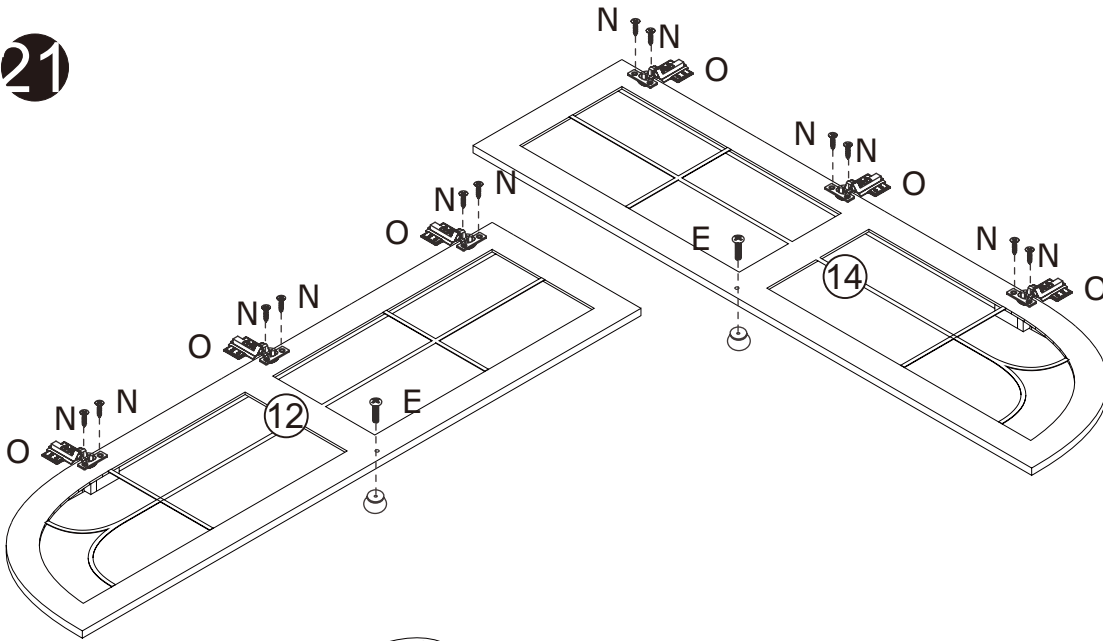





Tx5

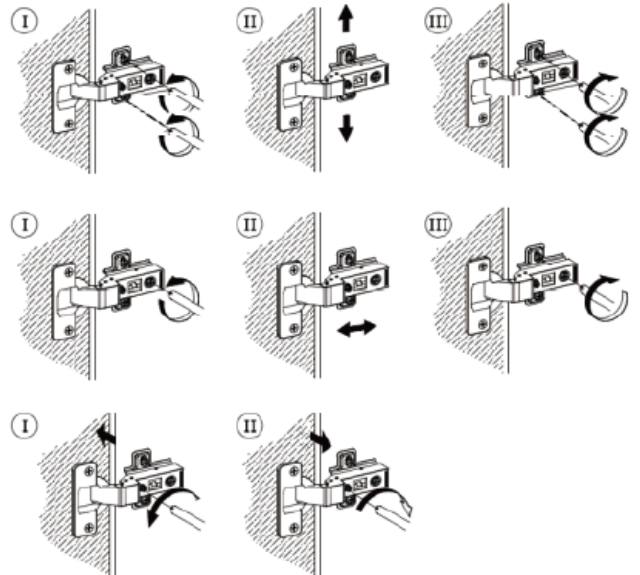
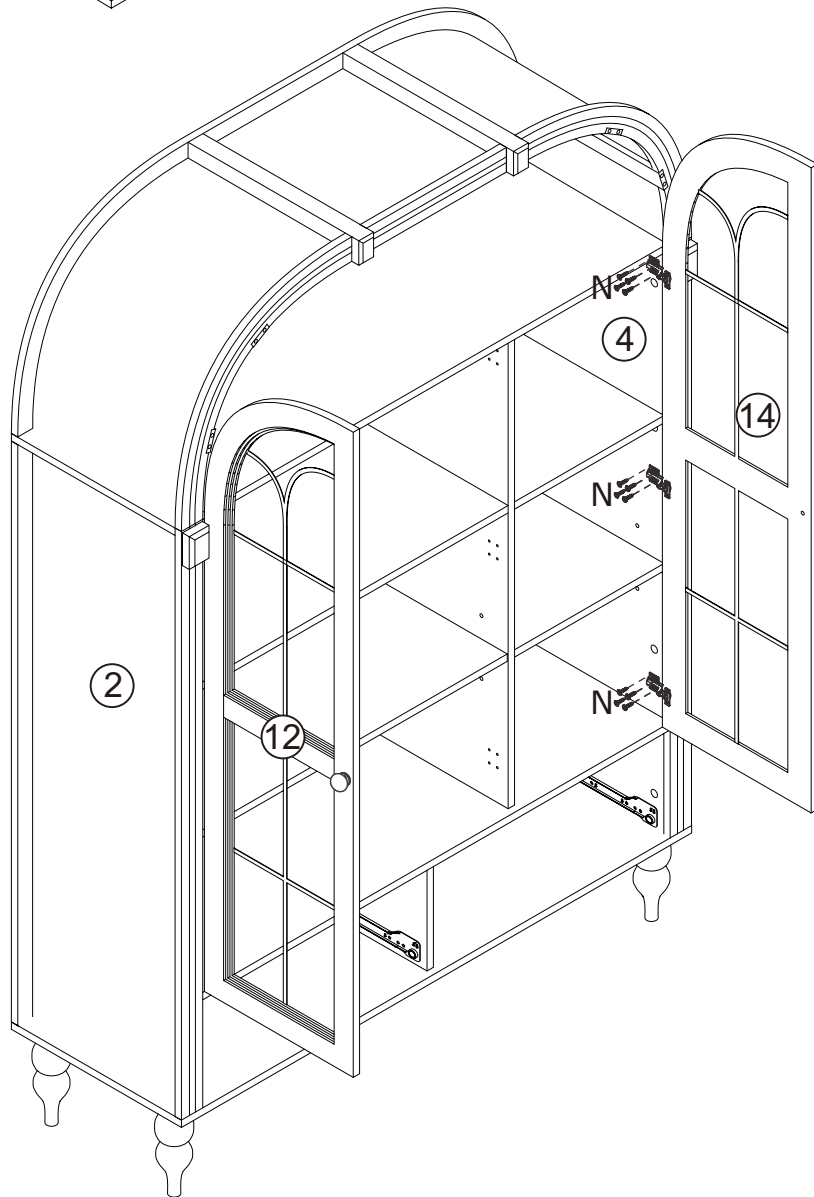
Px27



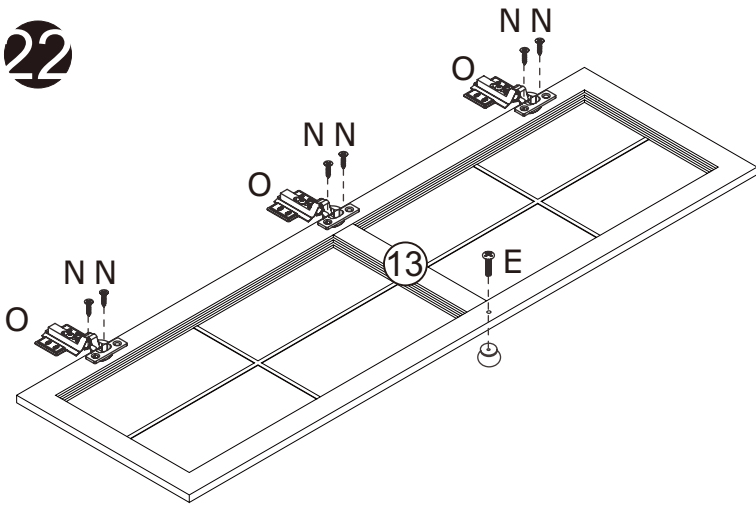
21



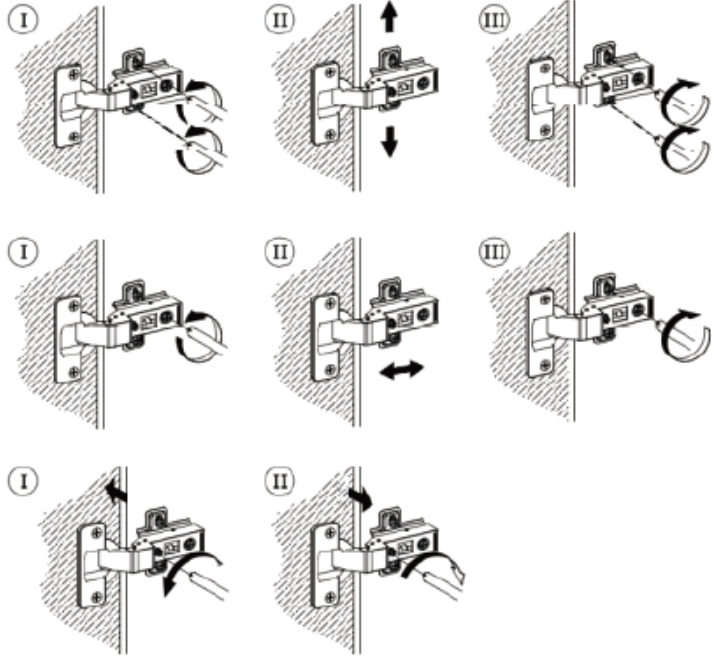
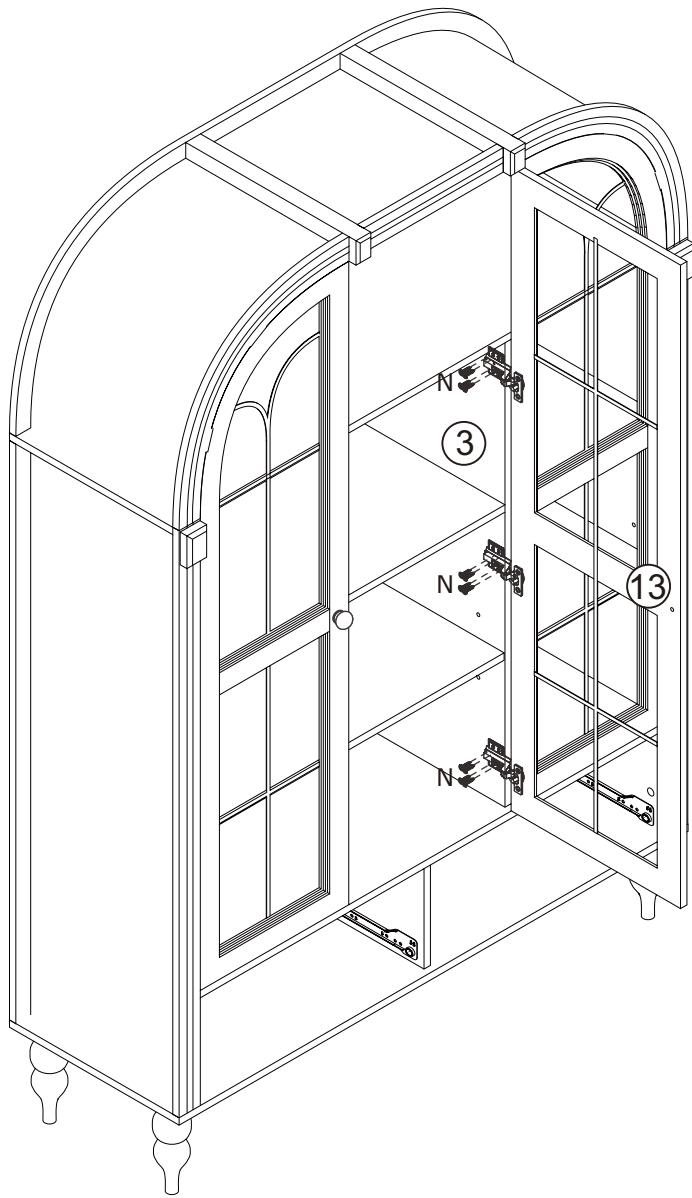
- E x2

- N x36

- O x6


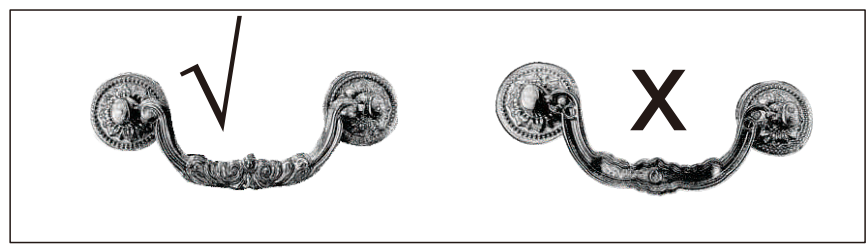
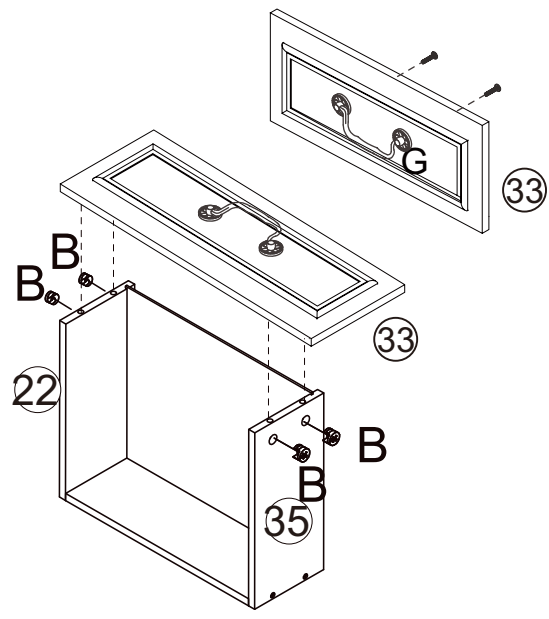
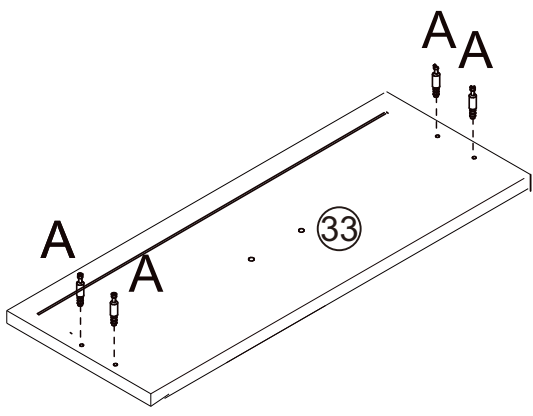
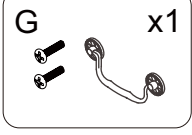
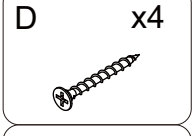
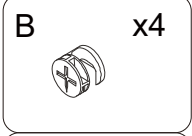
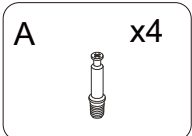
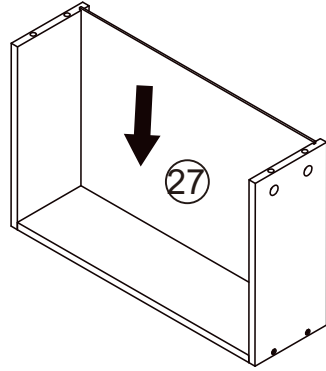
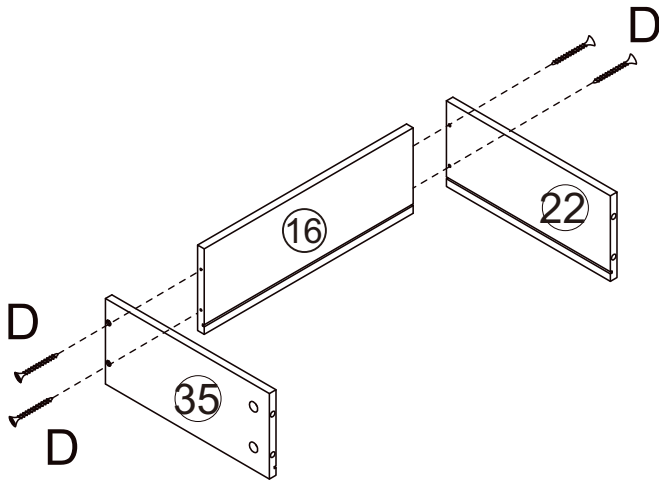


22

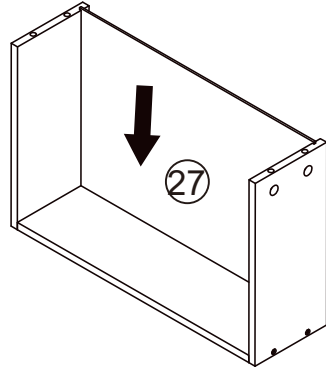
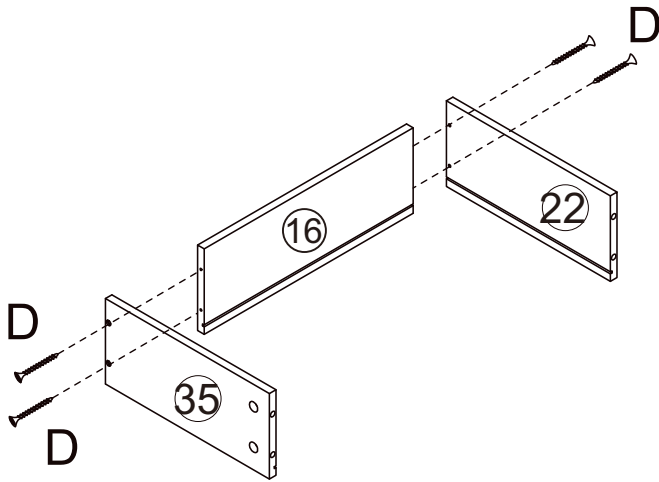






- E x1
- N x18
- O x3

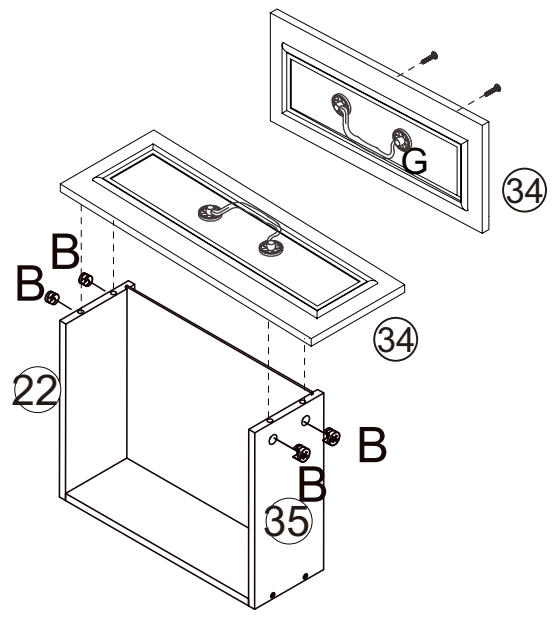
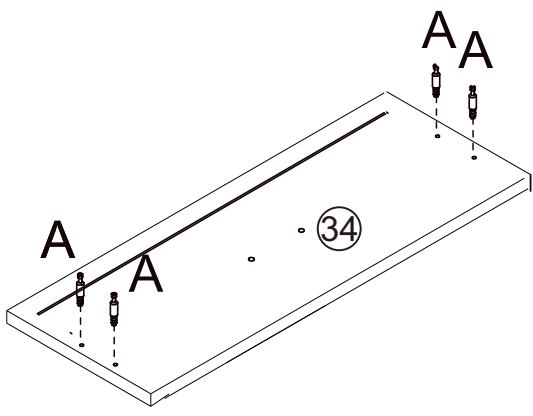




24



- A x4 
- B x4 
- D x4 
- G x1 

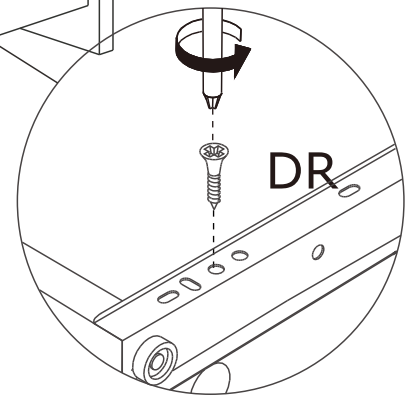
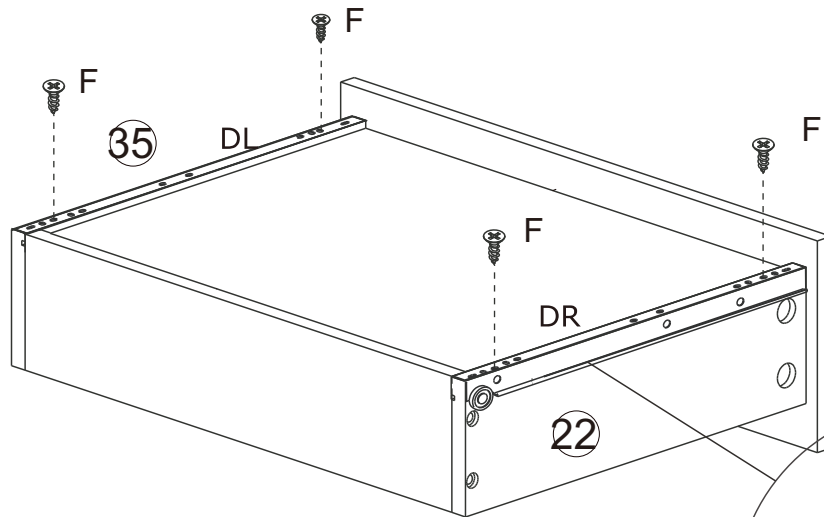
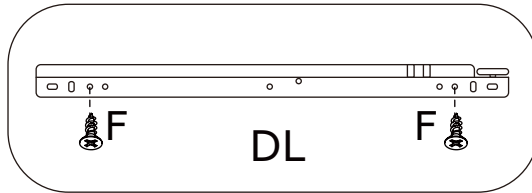
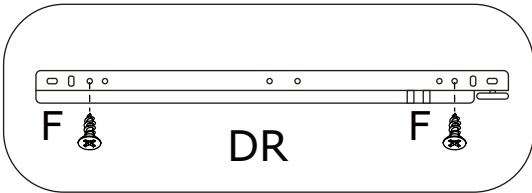


25

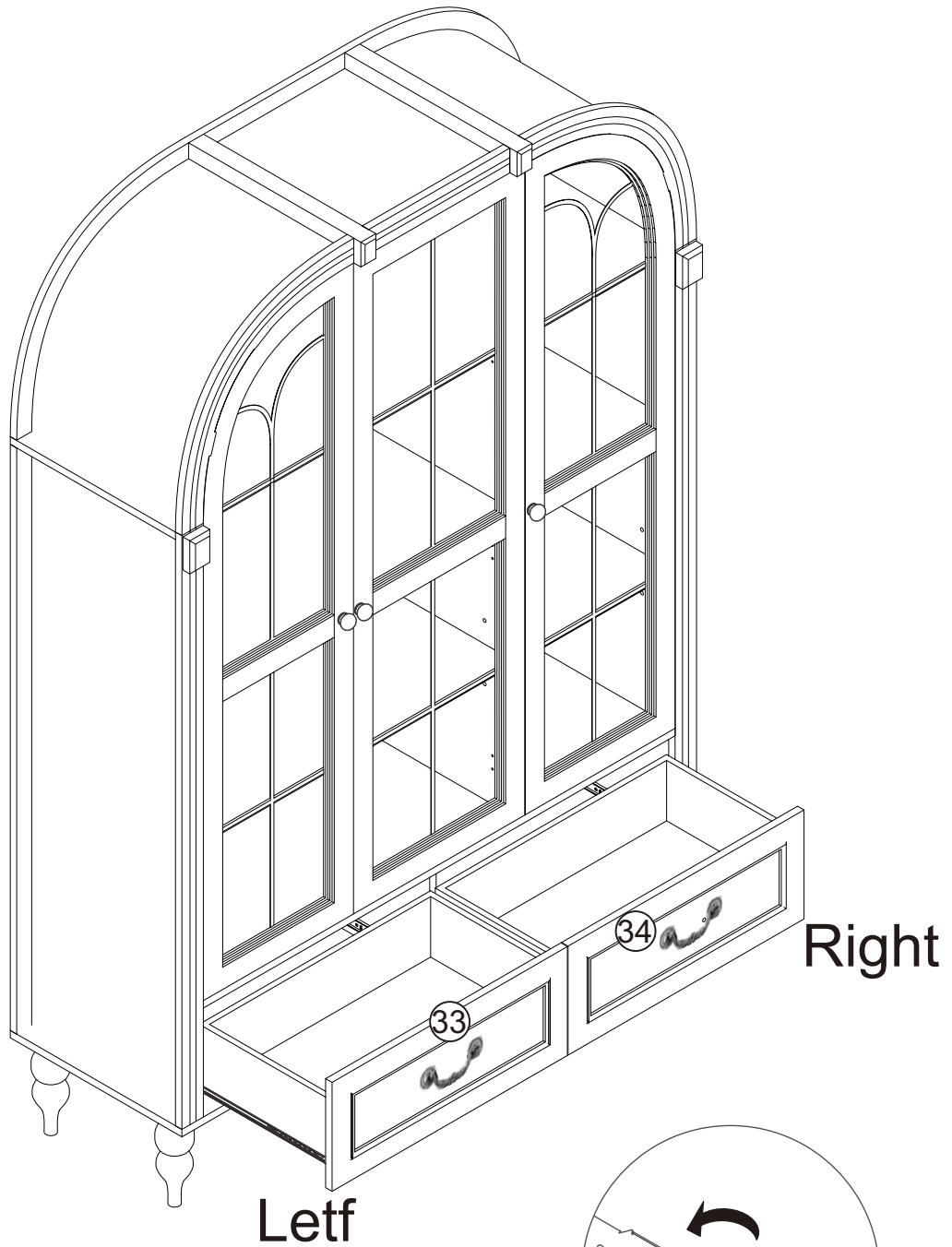
F x8

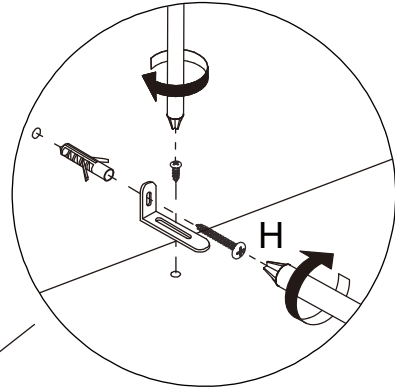
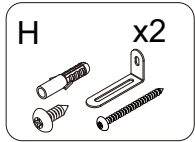
DL x2

DR x2

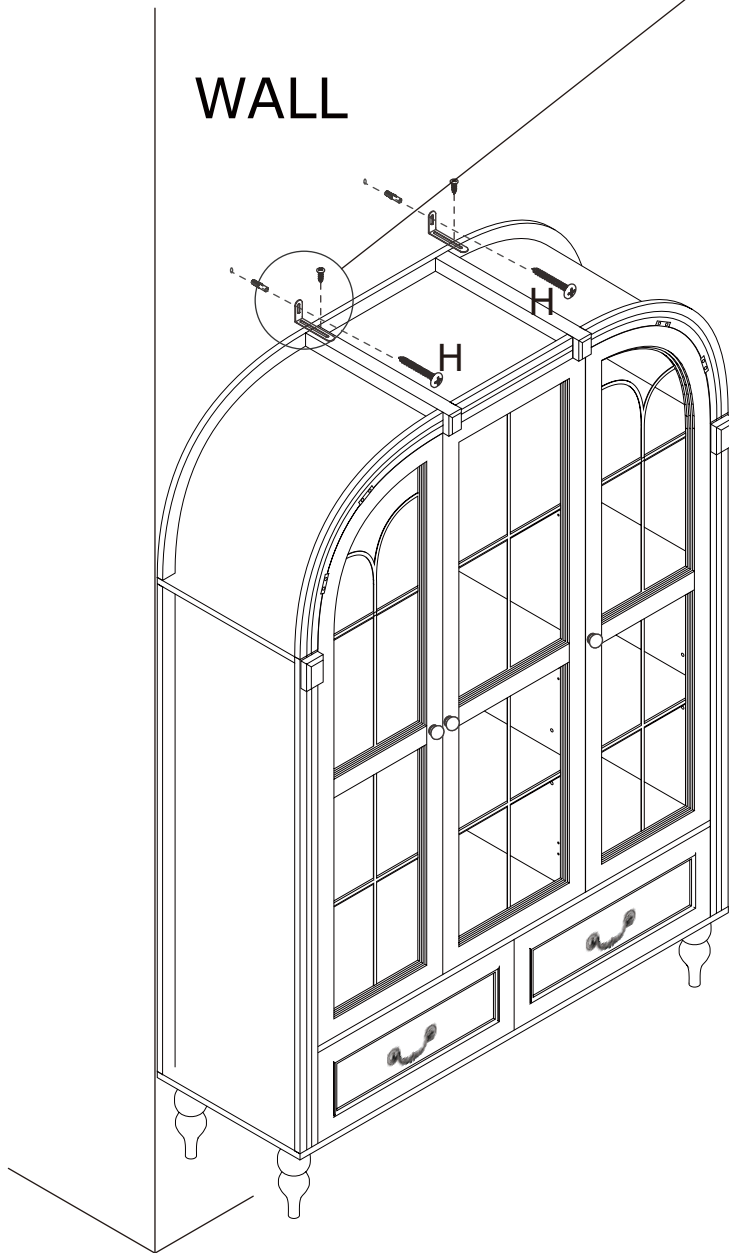


x2





WALL



- 1.
- 2.
- 3.
- 4.