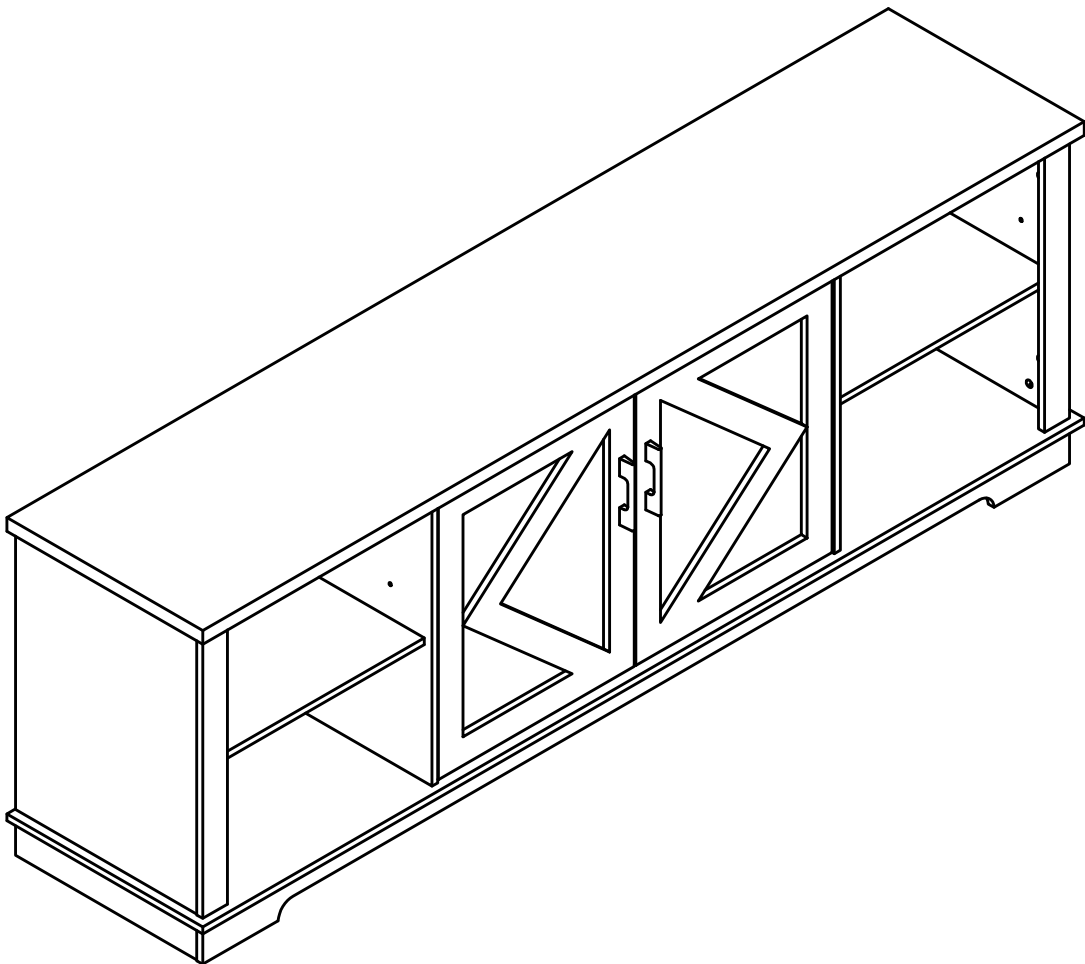
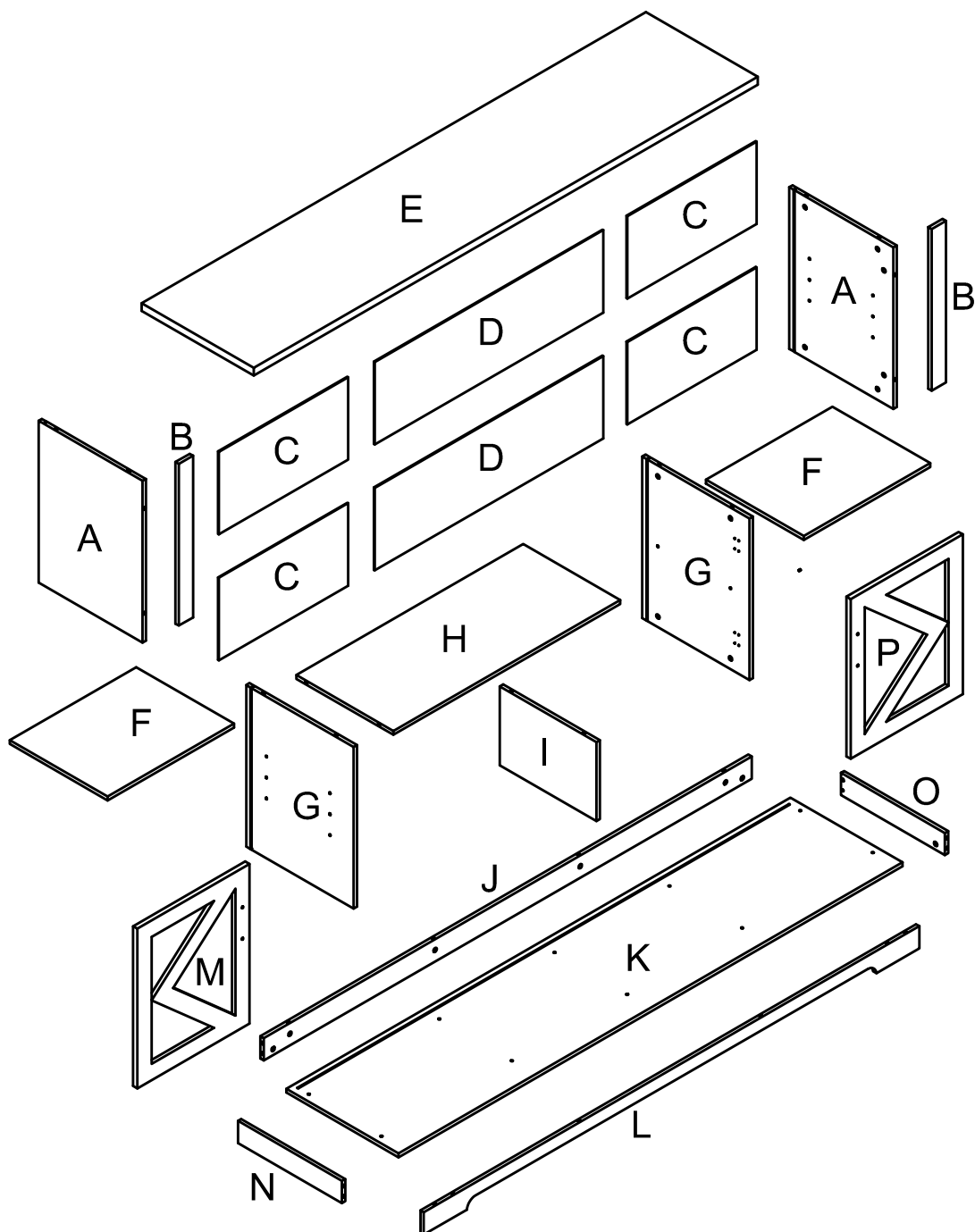

Assembly Instruction

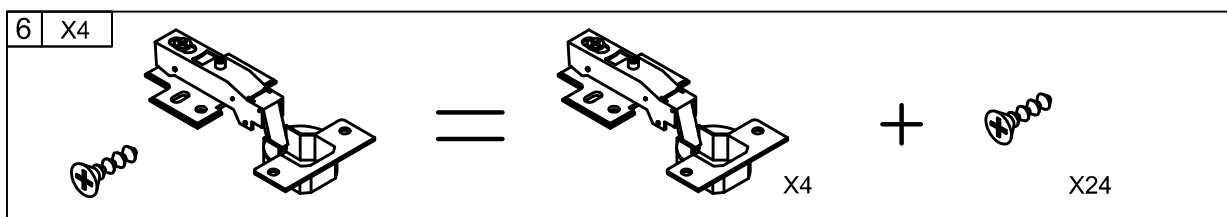
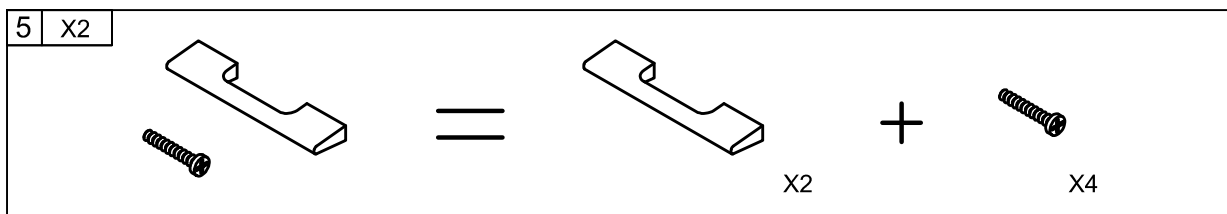
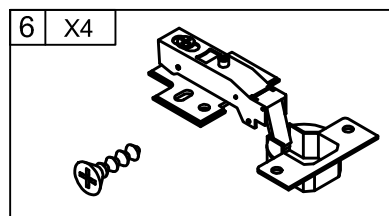
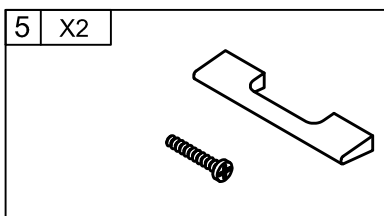
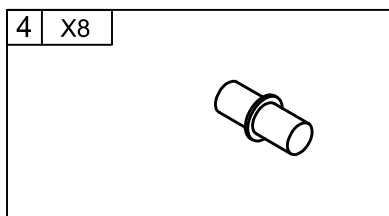
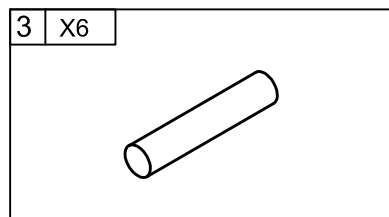
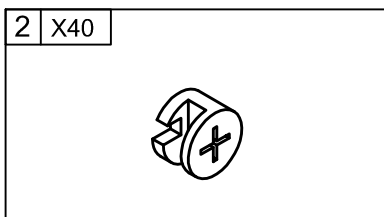
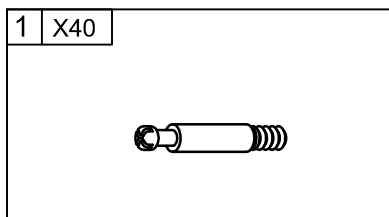
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PACKAGE CONTENTS

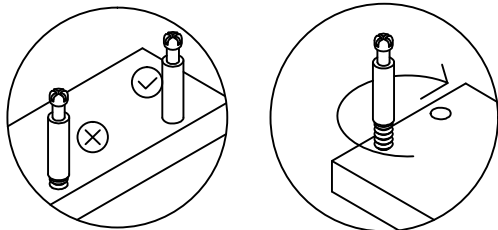


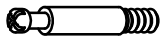

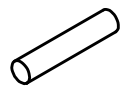
HARDWARE CONTENTS

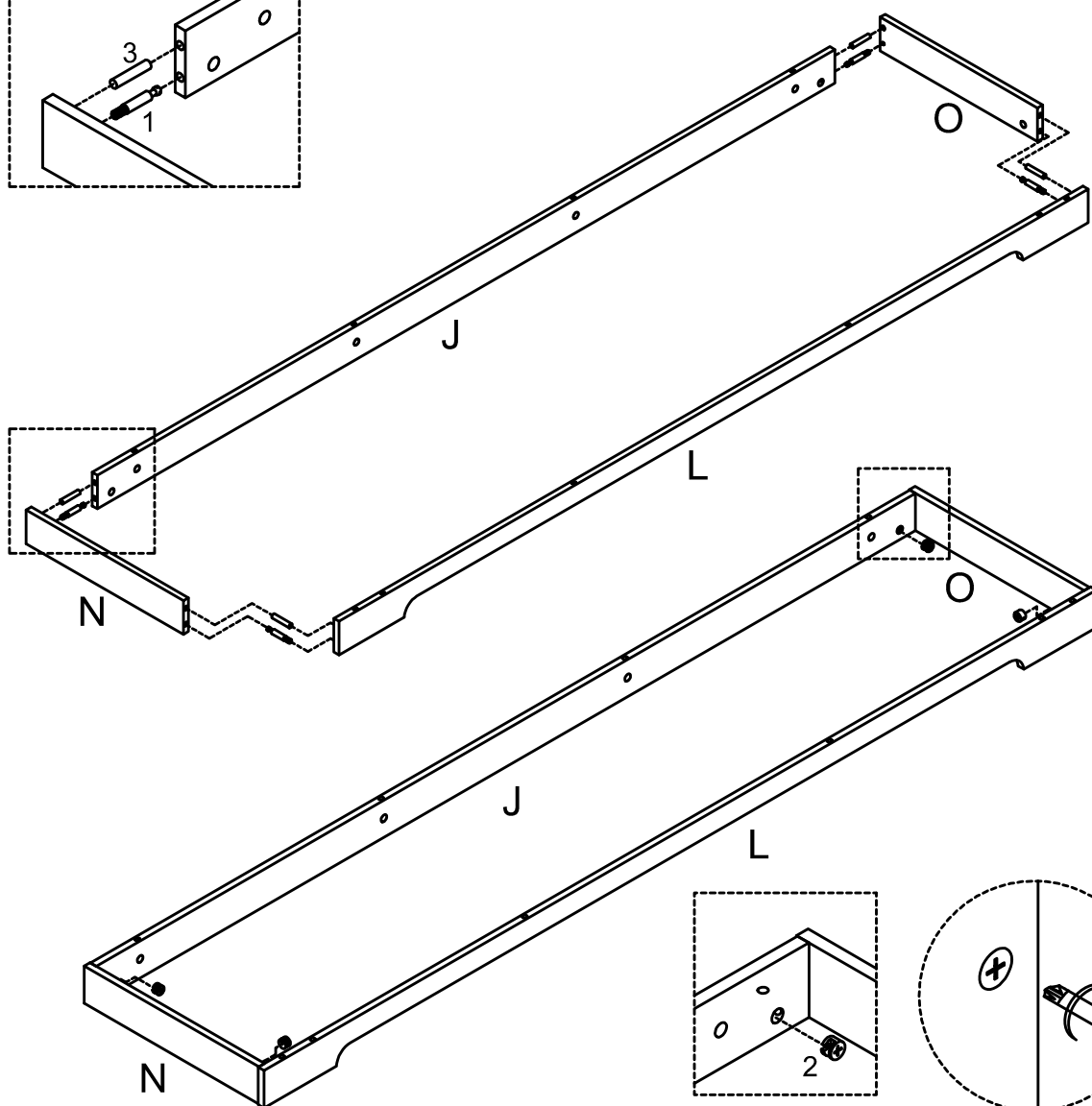
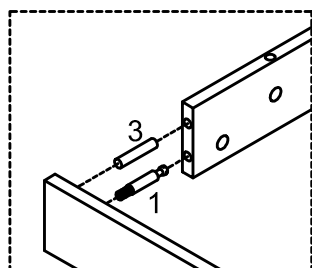


PACKAGE STEPS

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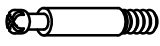

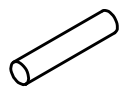


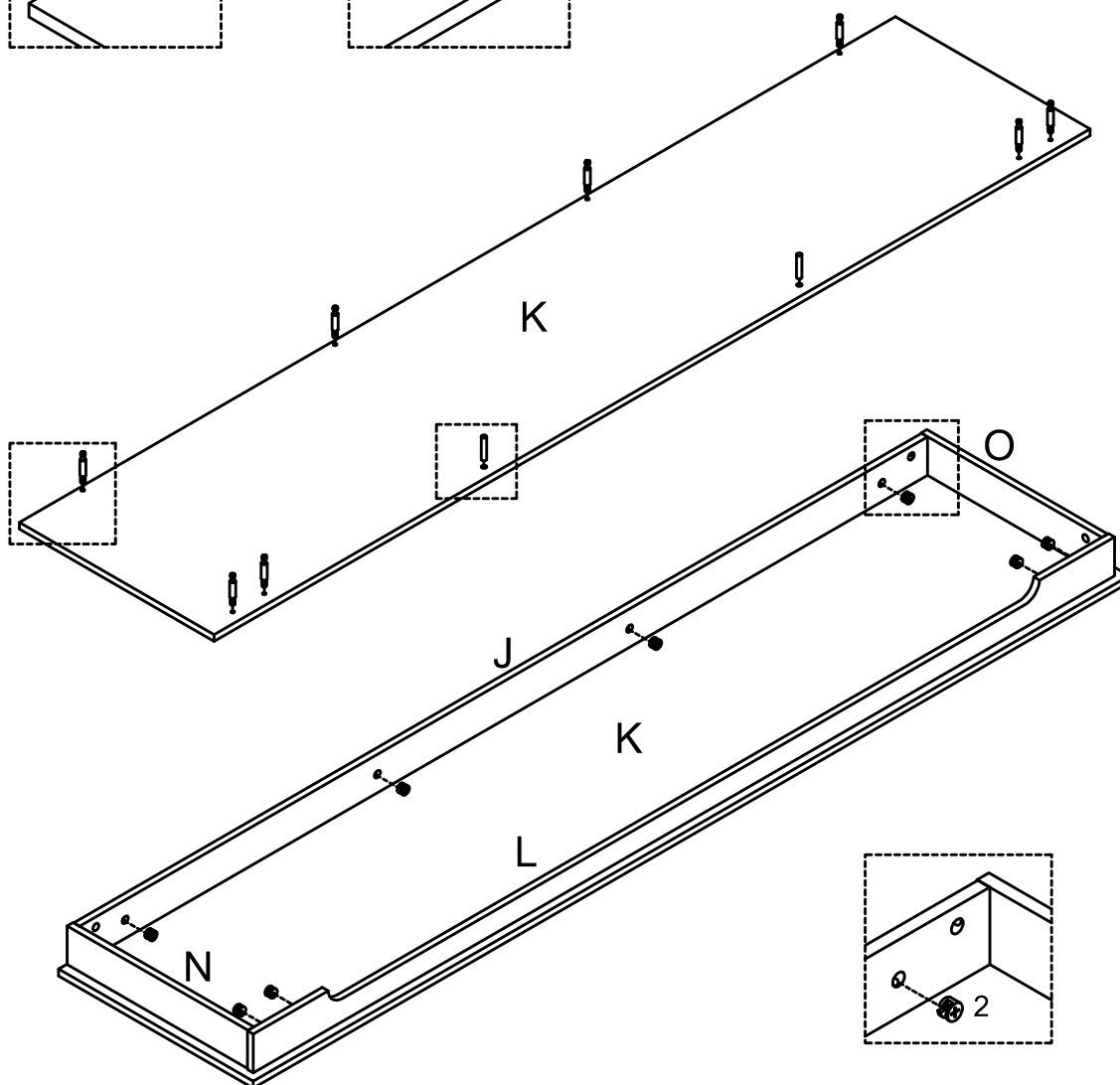
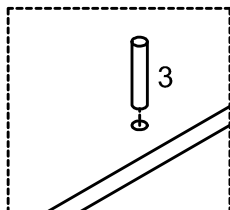
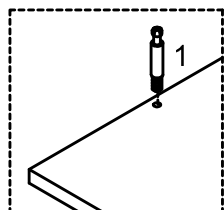
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|---|----|---|
| 1 | X4 |  |
| 2 | X4 |  |
| 3 | X4 |  |



PACKAGE STEPS

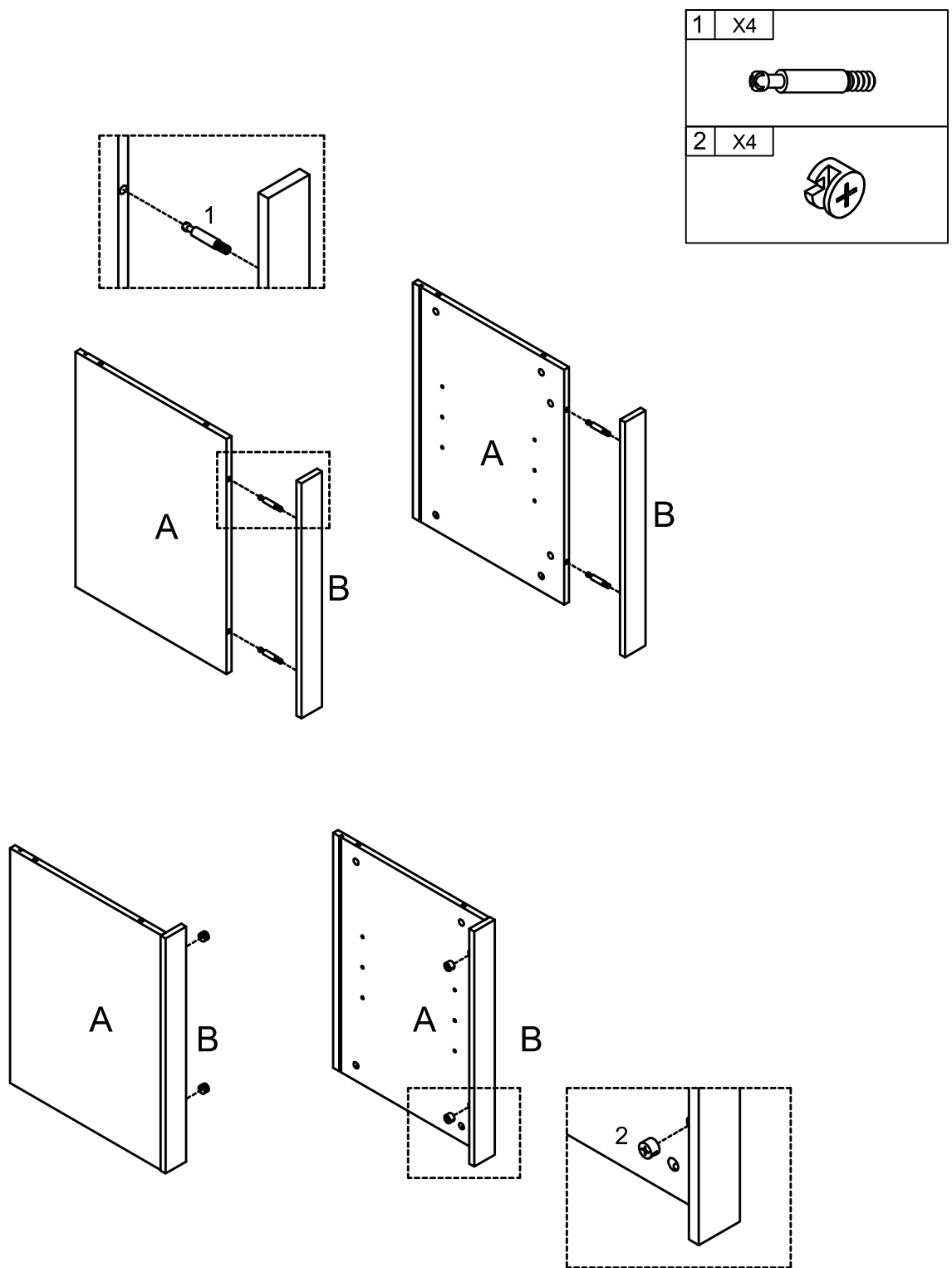
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| | | |
|---|----|---|
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| 2 | X8 |  |
| 3 | X2 |  |



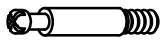

PACKAGE STEPS

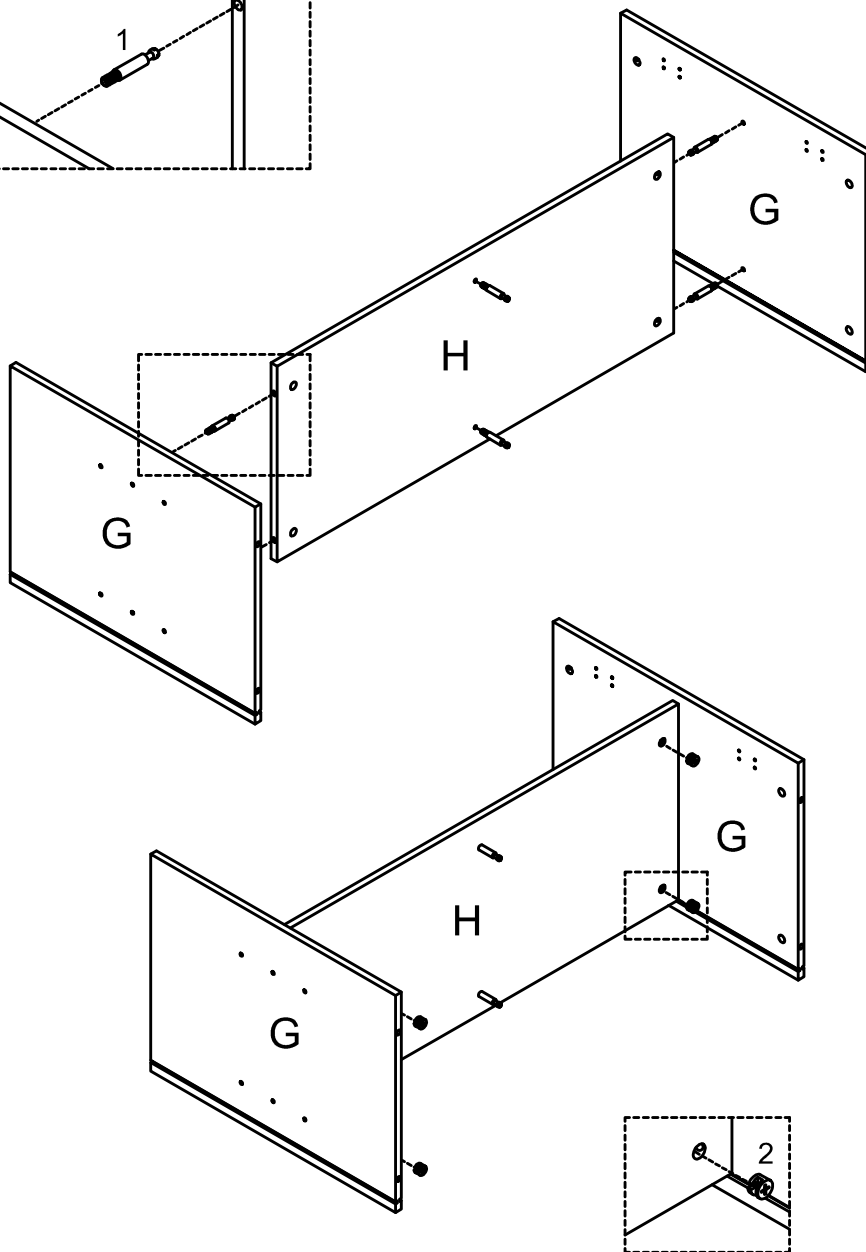
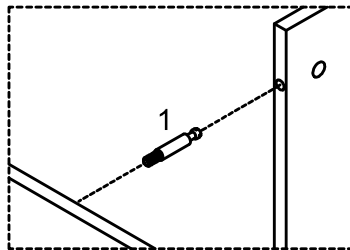
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PACKAGE STEPS

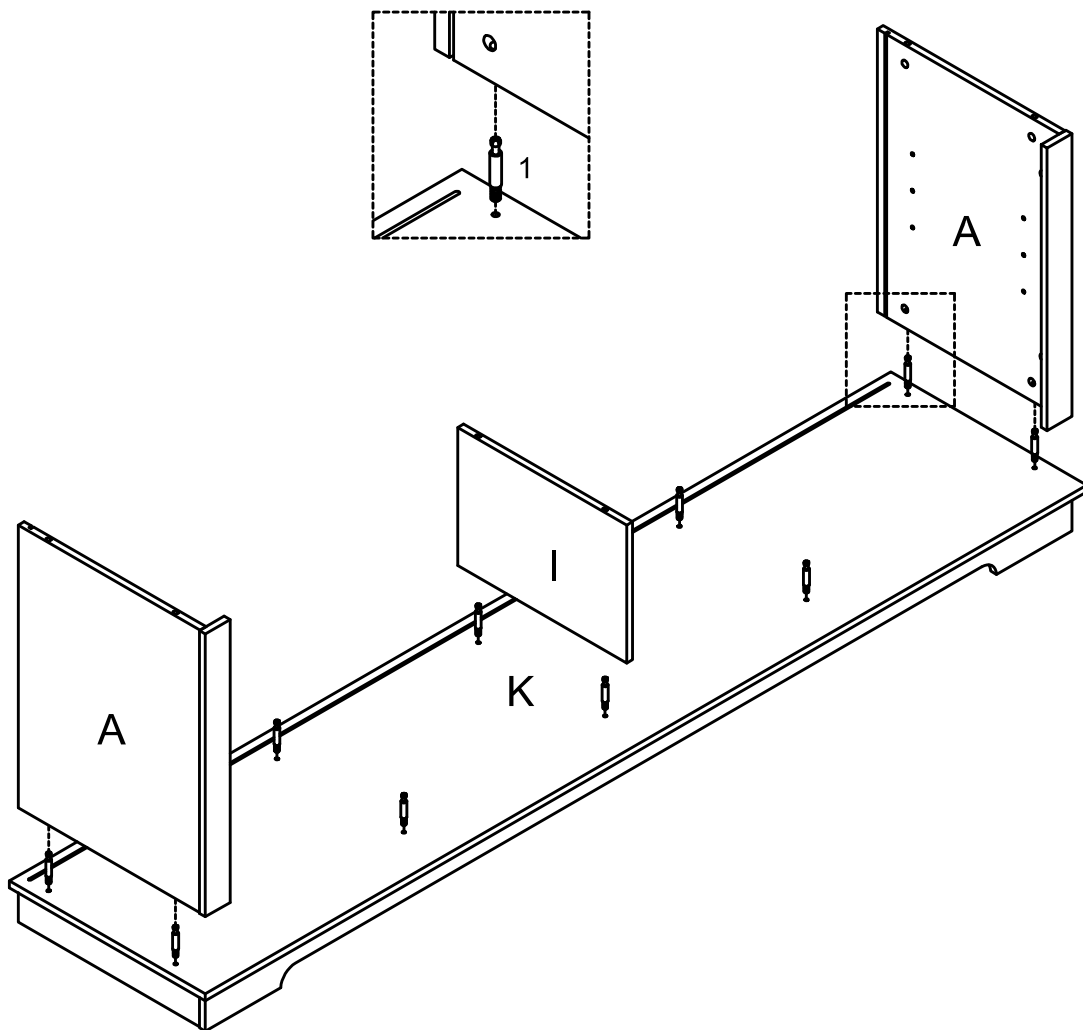
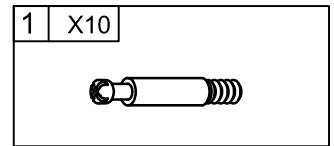
4.

| | |
|---|----|
| 1 | X6 |
|  | |
| 2 | X4 |
|  | |



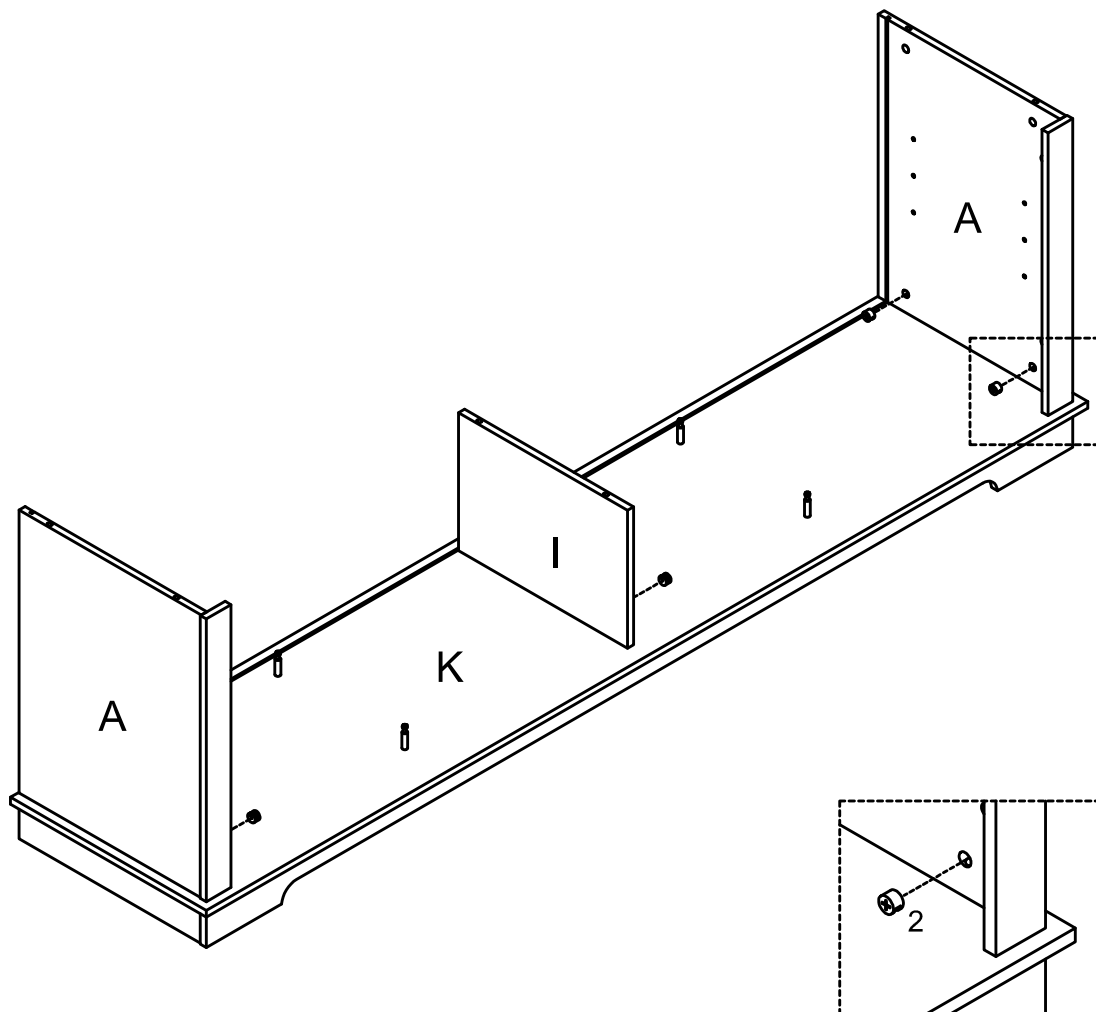
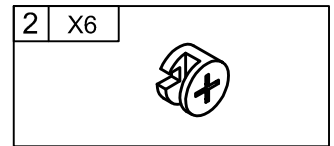
PACKAGE STEPS

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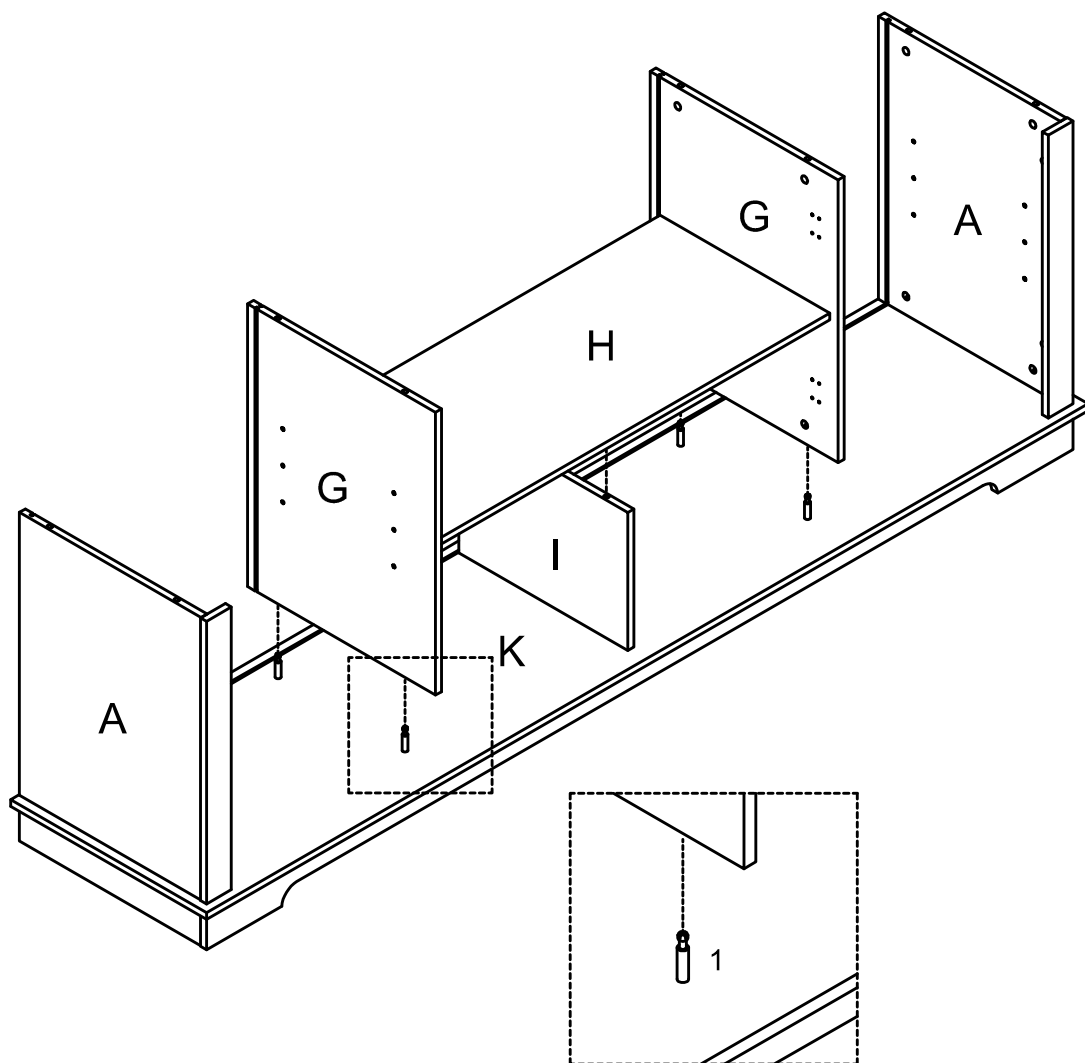
PACKAGE STEPS

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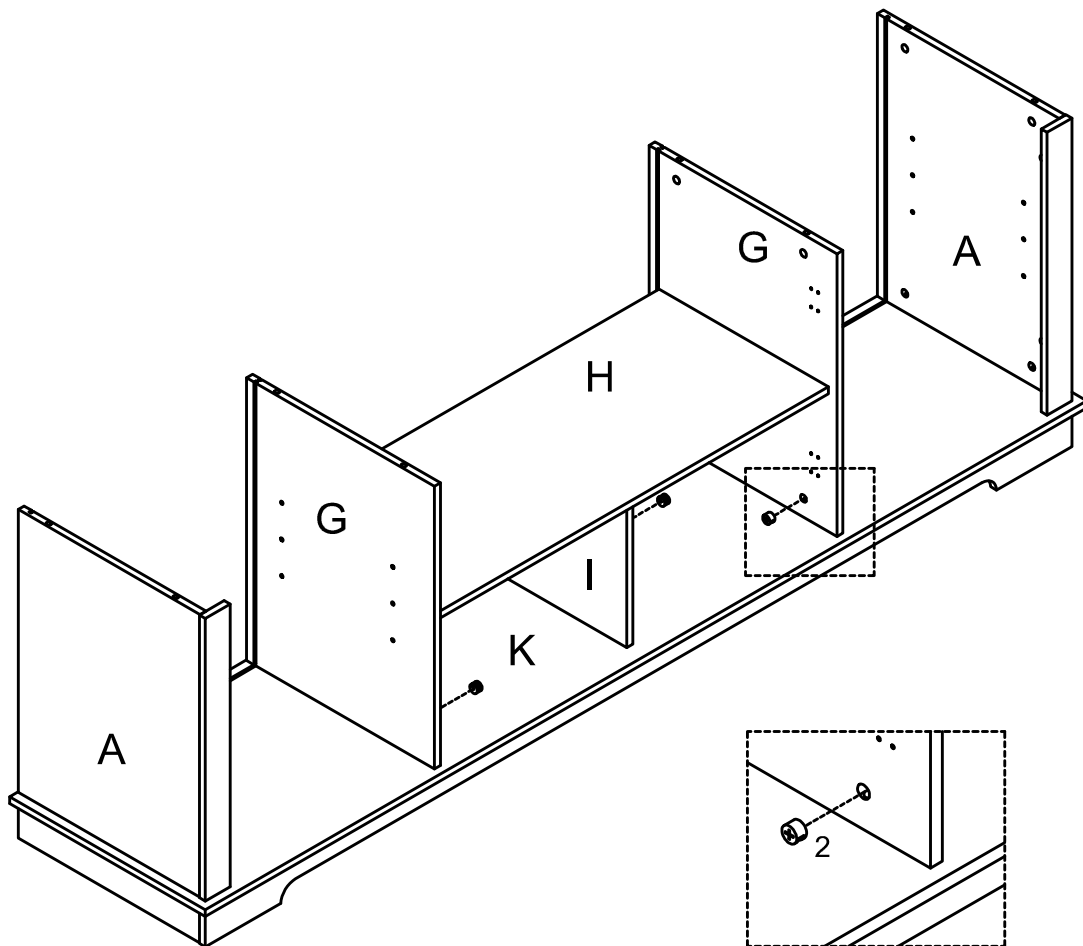
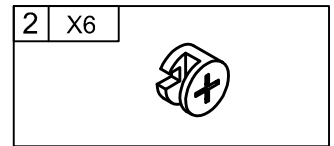
PACKAGE STEPS

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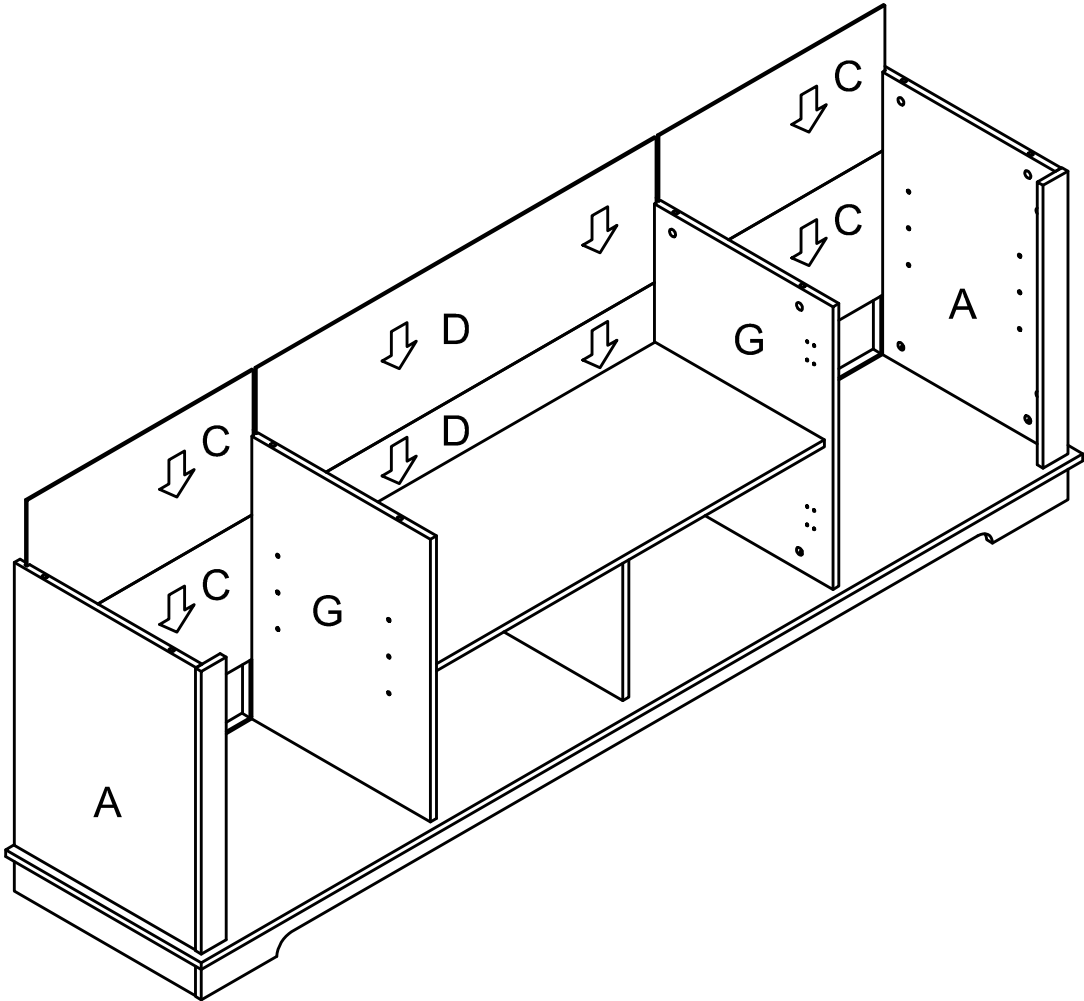
PACKAGE STEPS

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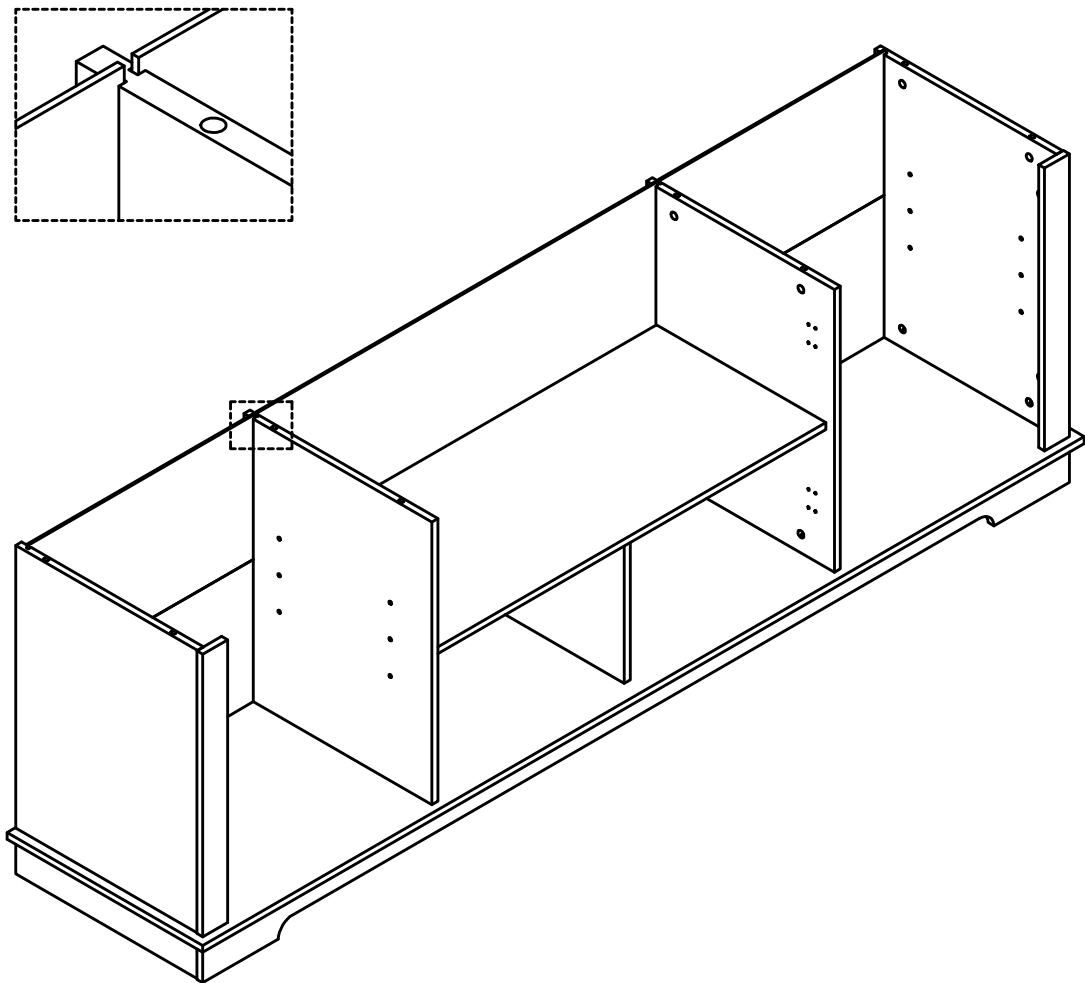
PACKAGE STEPS

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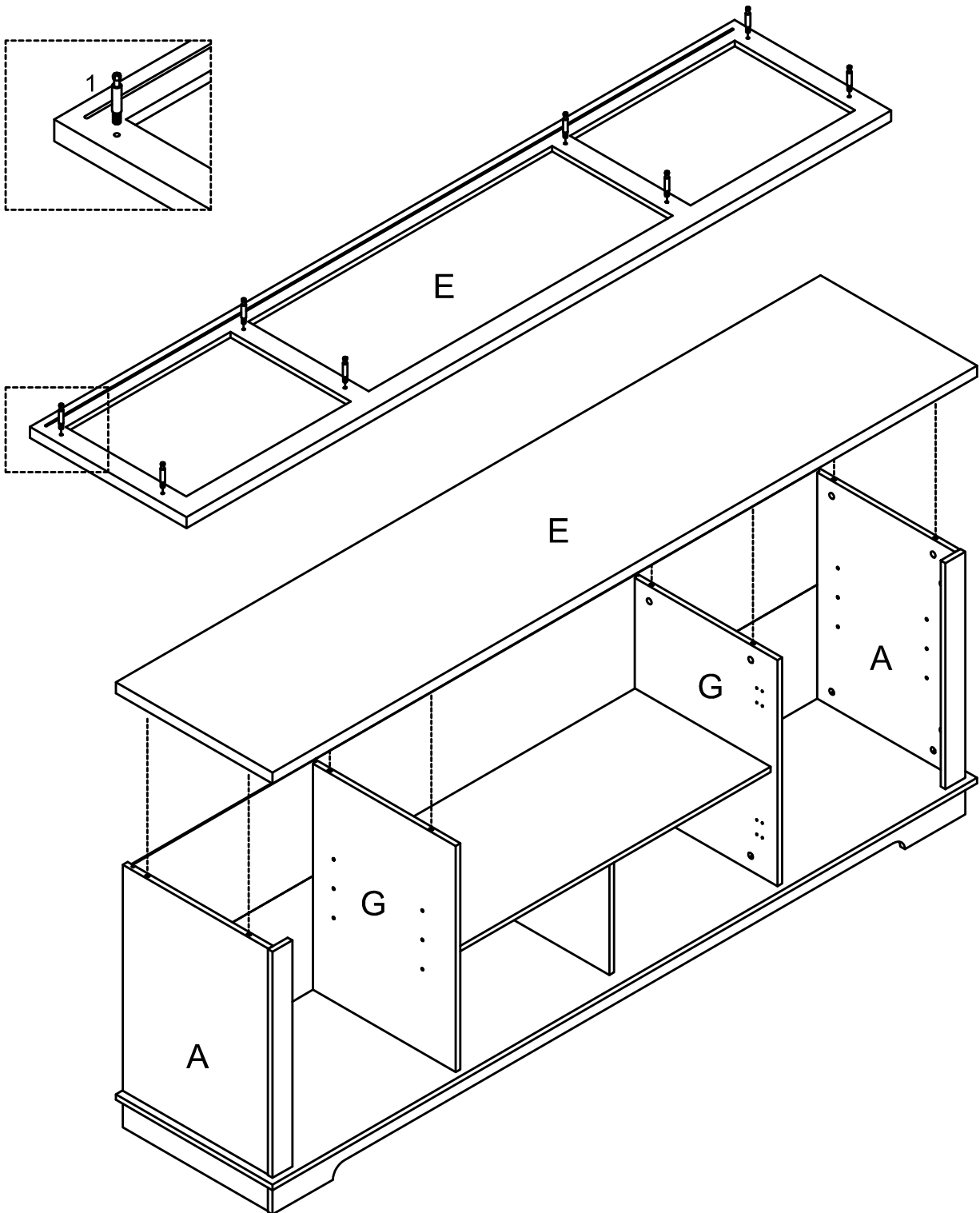
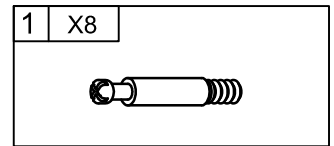
PACKAGE STEPS

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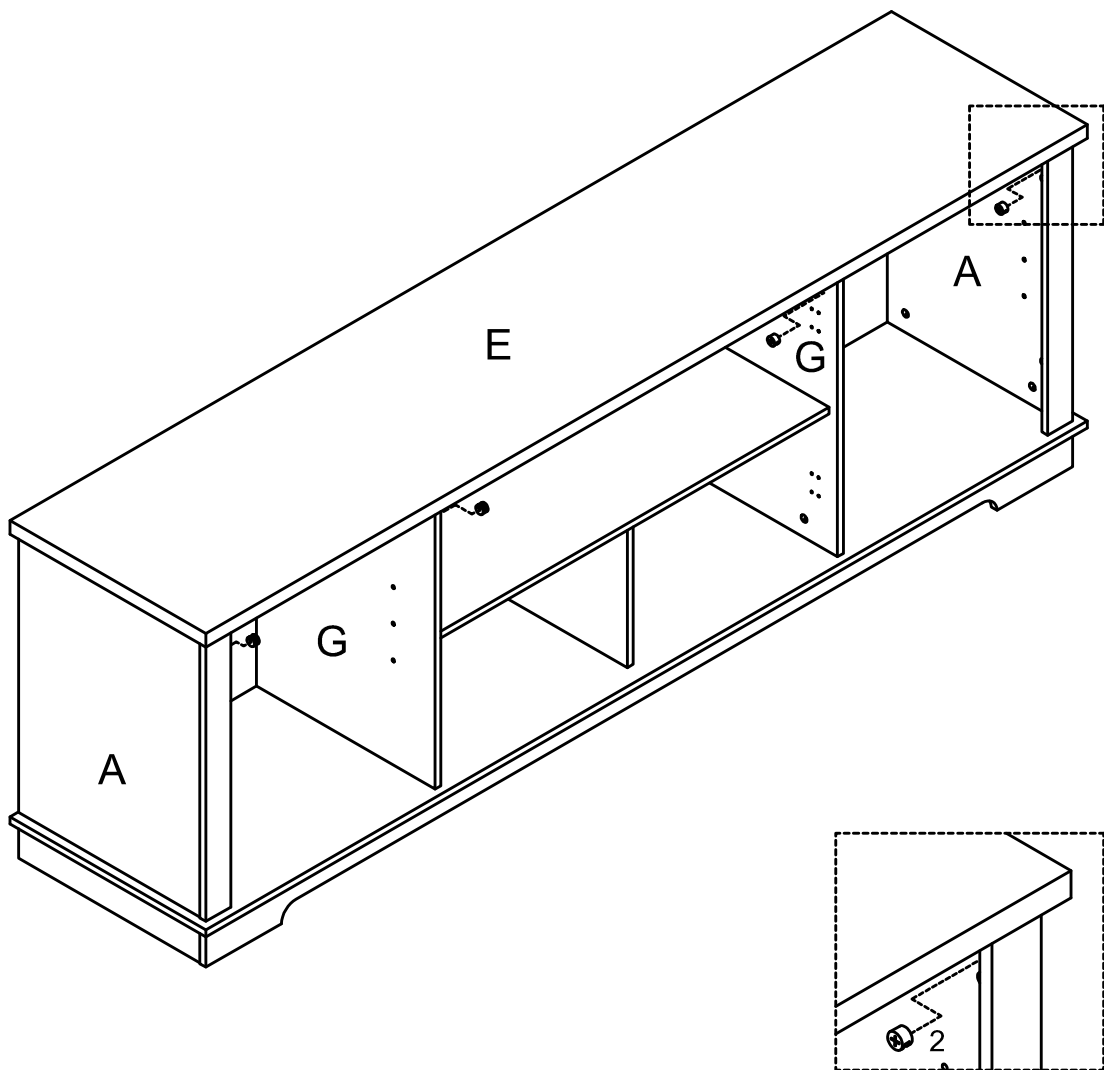
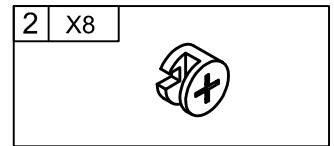
PACKAGE STEPS

11.



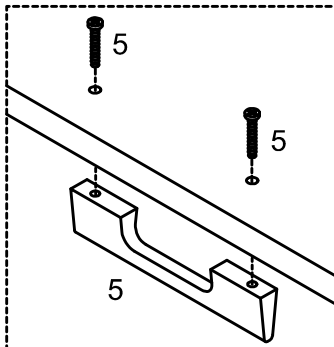
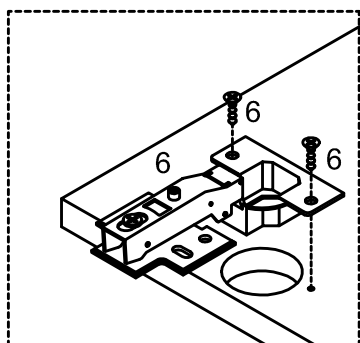
PACKAGE STEPS

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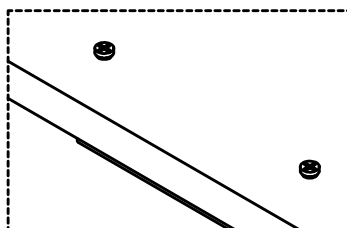
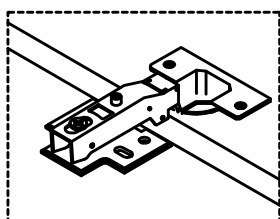
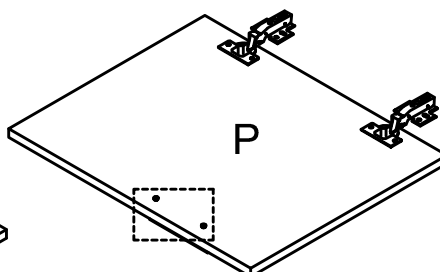
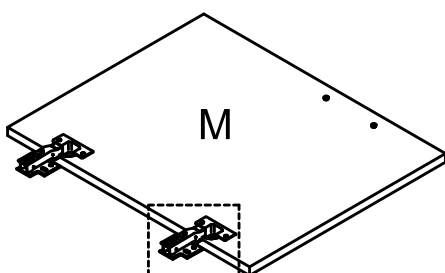
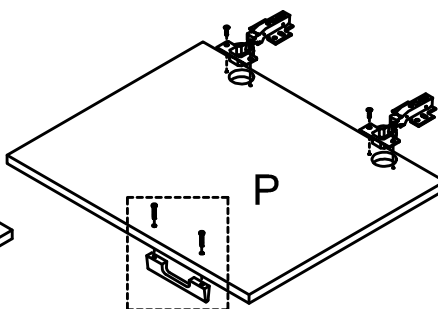
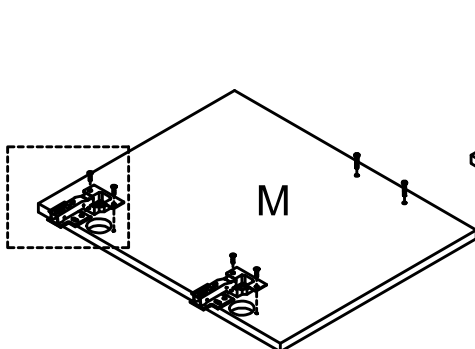


PACKAGE STEPS

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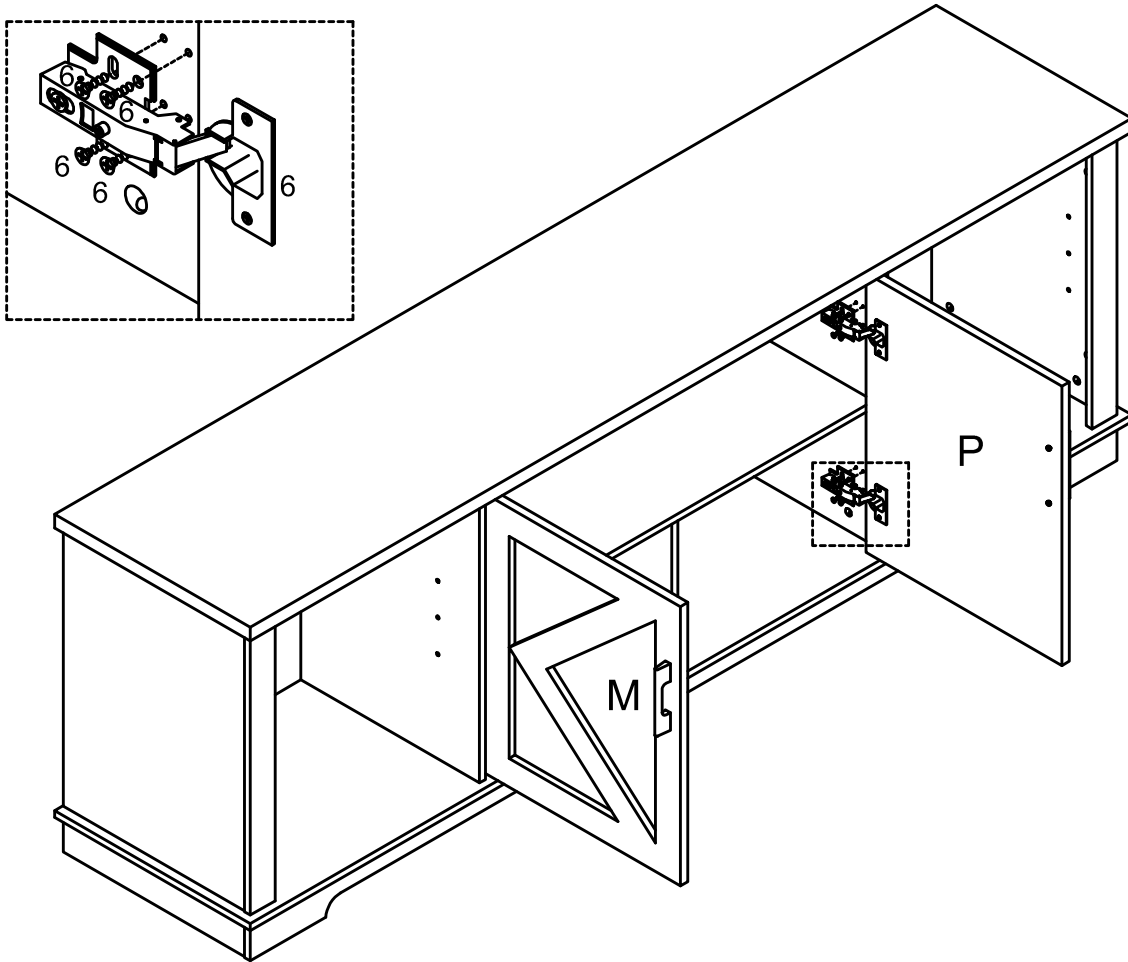


| | | |
|---|----|--|
| 5 | X2 | |
| 6 | X4 | |



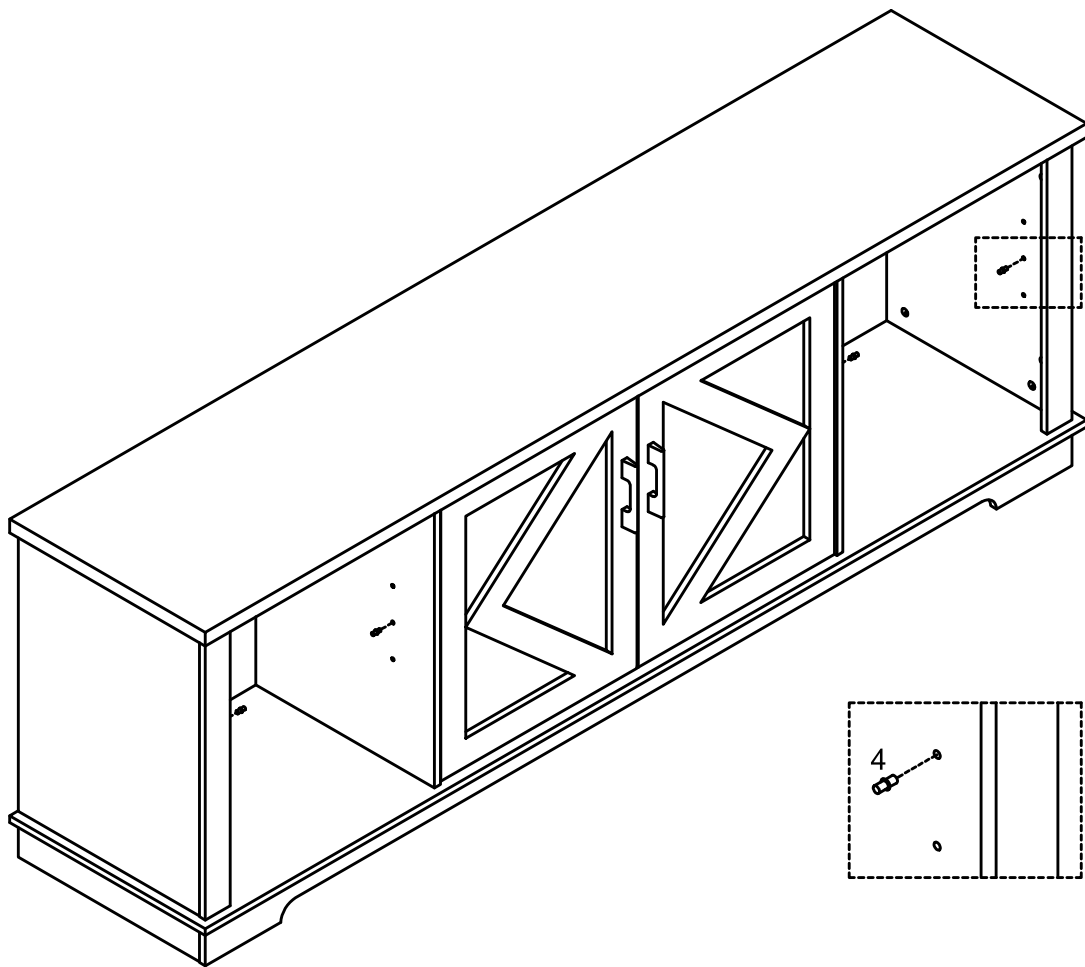
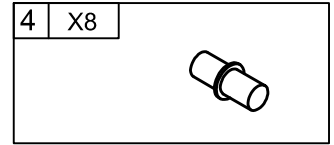
PACKAGE STEPS

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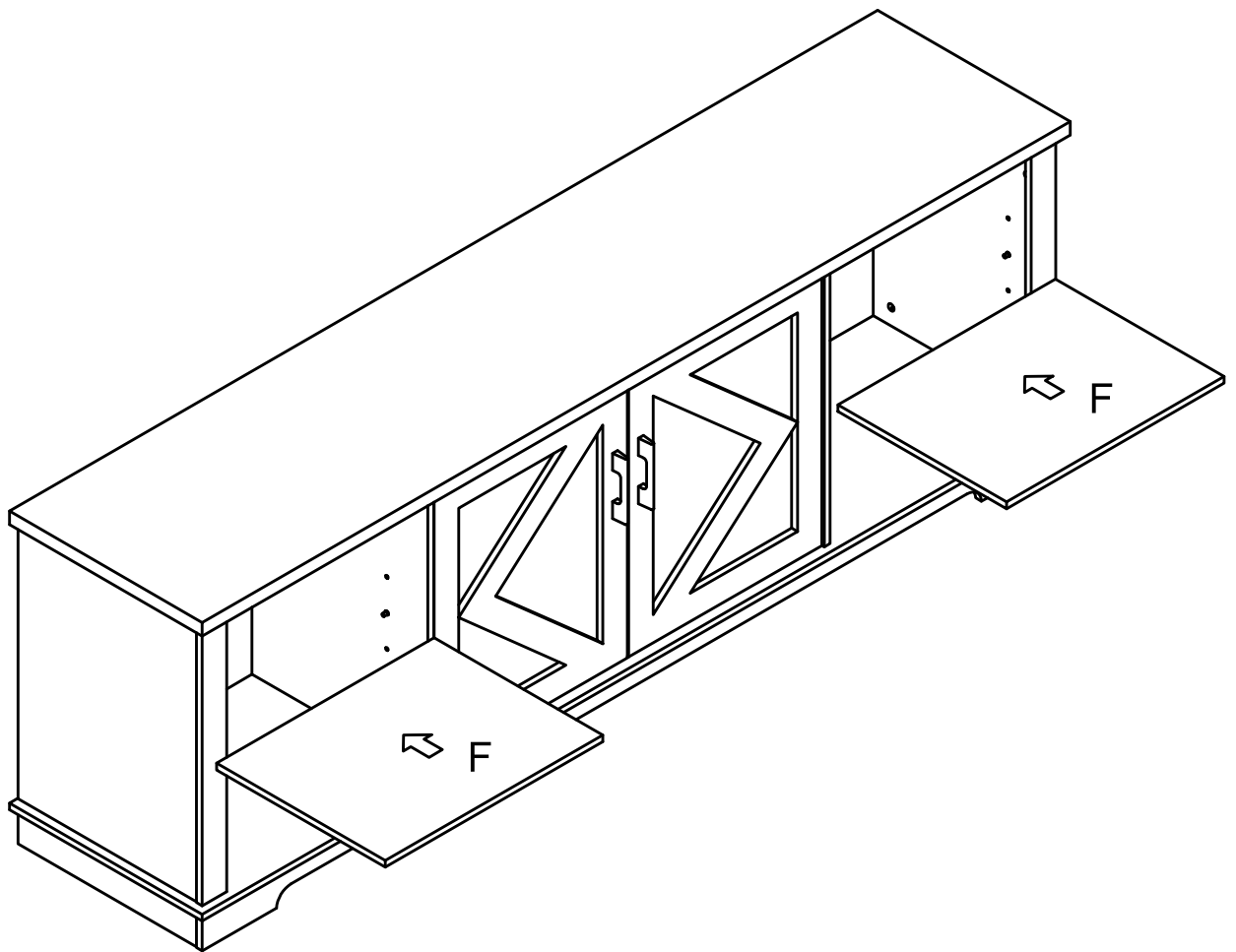
PACKAGE STEPS

15.



PACKAGE STEPS

16.



PACKAGE STEPS

17.

