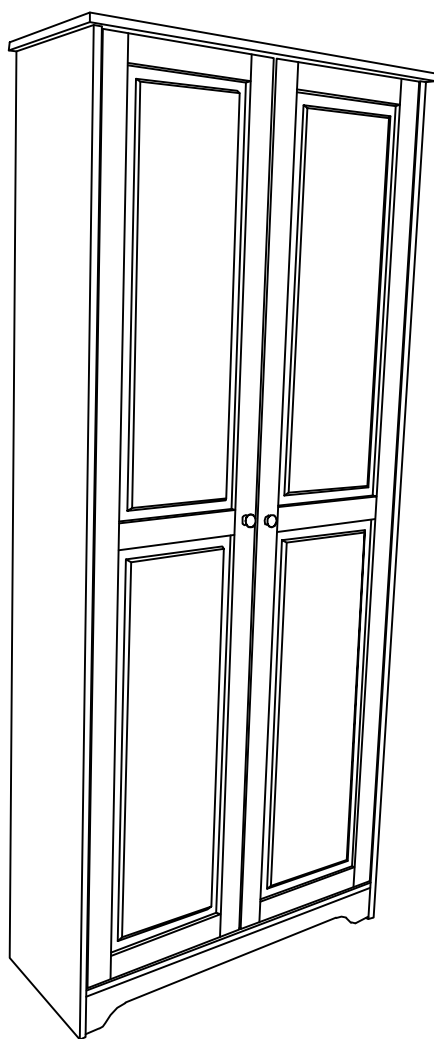


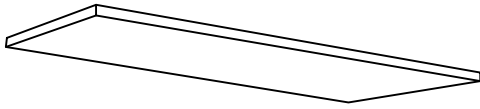
# ASSEMBLY INSTRUCTION



# Components

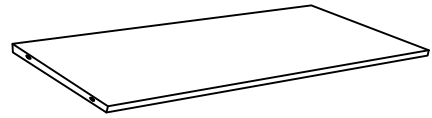
**A**

**x1**



**B**

**x1**



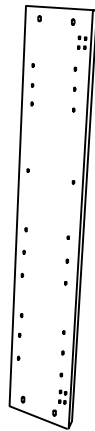
**C**

**x1**



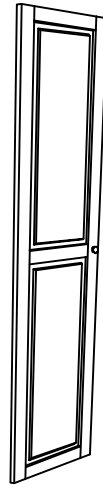
**D**

**x1**



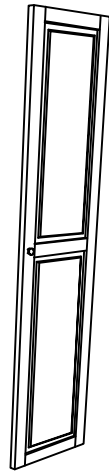
**E**

**x1**



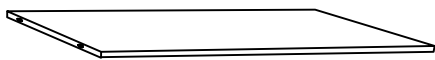
**F**

**x1**



**G**

**x1**



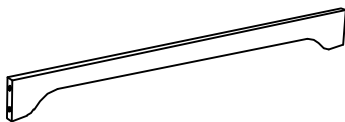
**H**

**x3**



**I**

**x1**



**J**

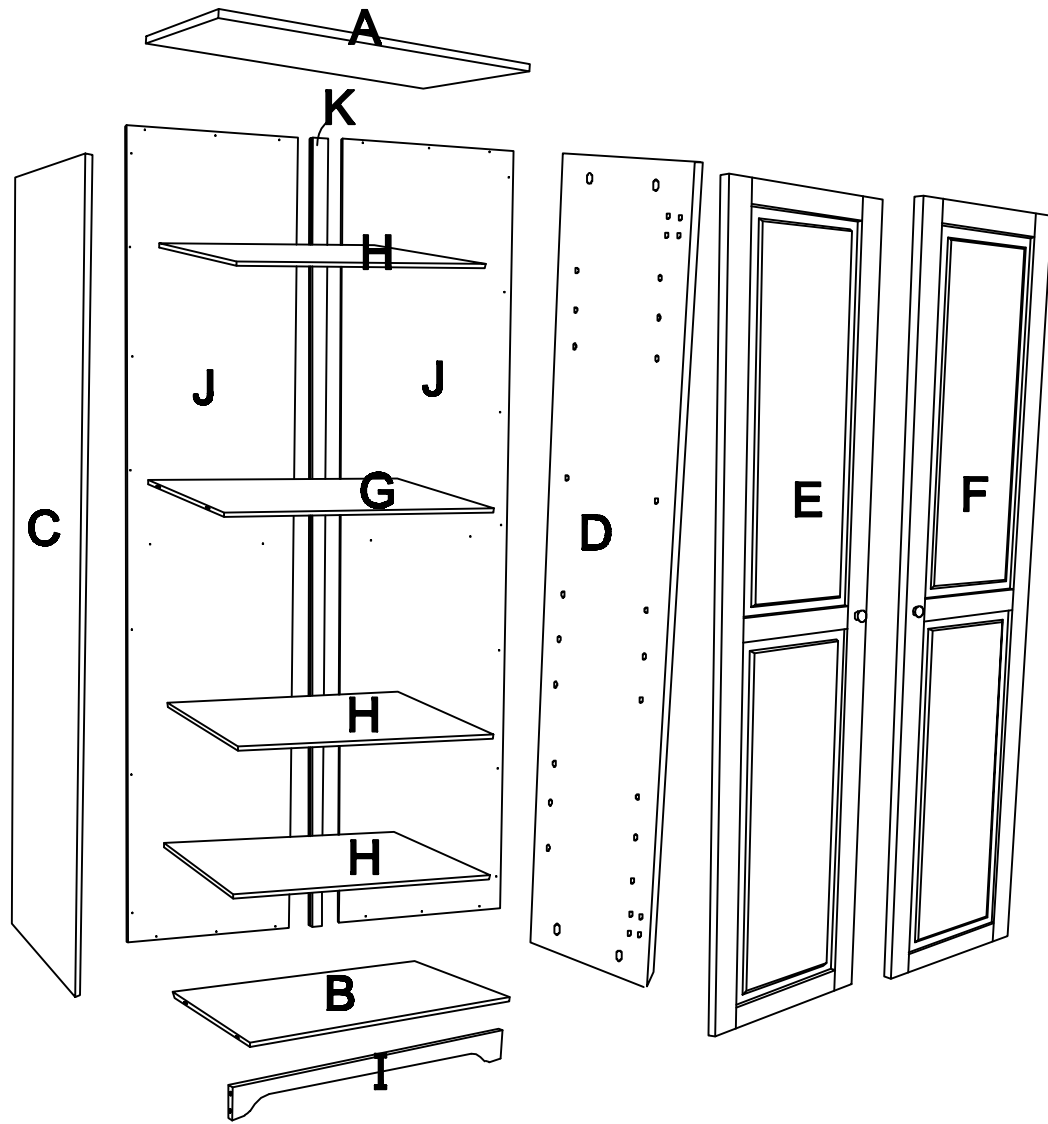
**x2**



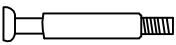





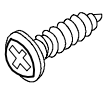
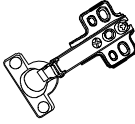
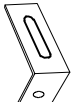


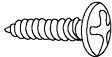

**K**

**x1**

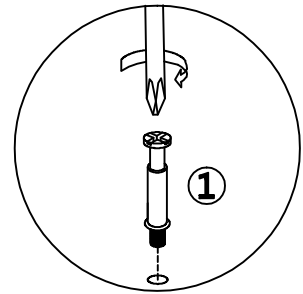
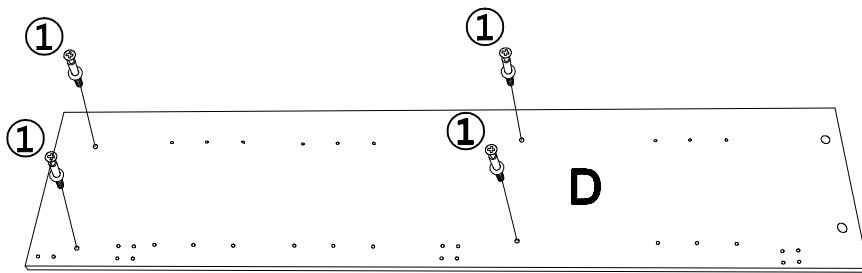
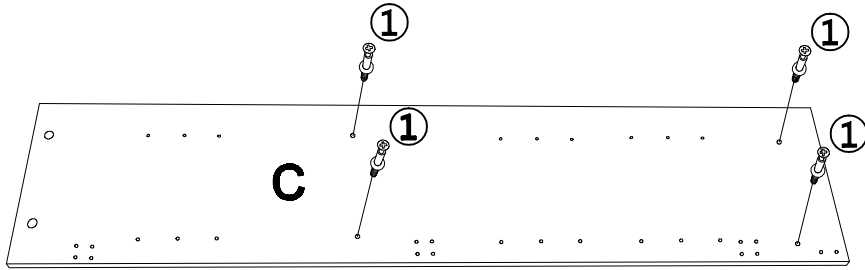
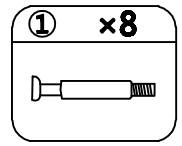




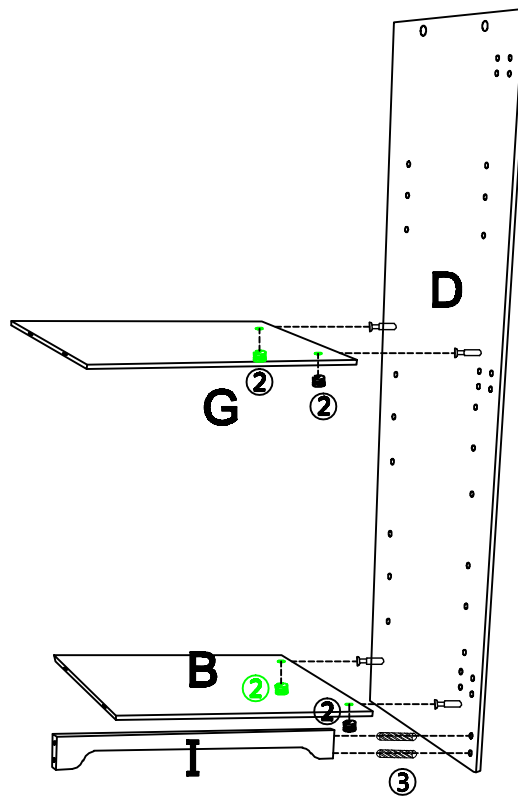
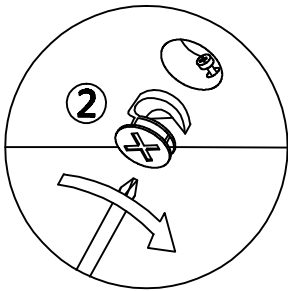
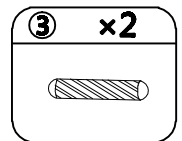
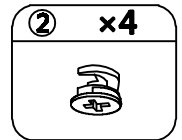
# Hardware

① ×12 	② ×12 	③ ×4 	④ ×36 	⑤ ×2 
⑥ ×2 	⑦ ×30 	⑧ ×6 	⑨ ×2 	⑩ ×2 
⑪ ×2 	⑫ ×2 	⑬ ×12 		

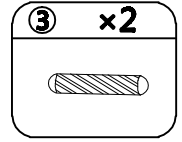
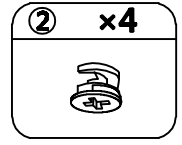
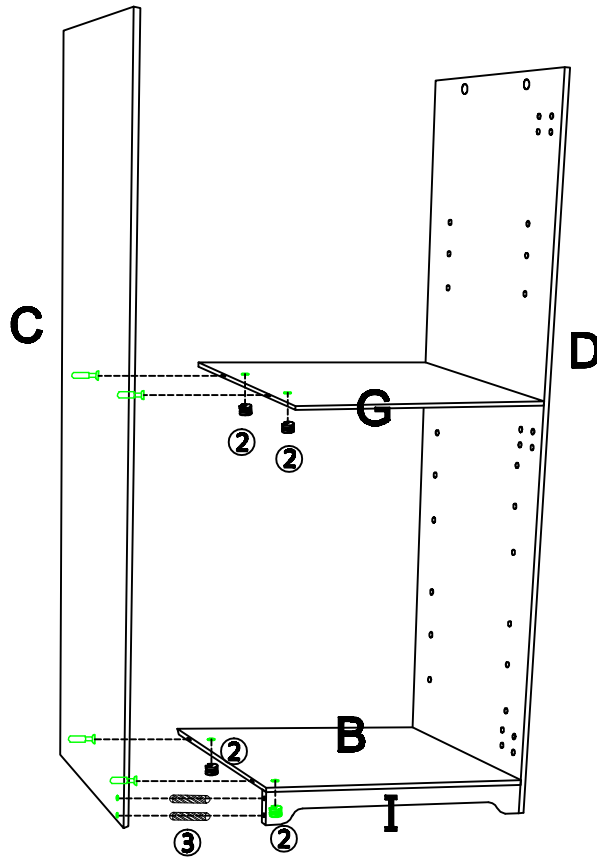
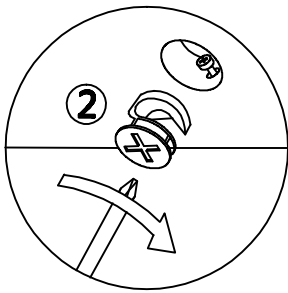
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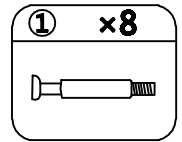
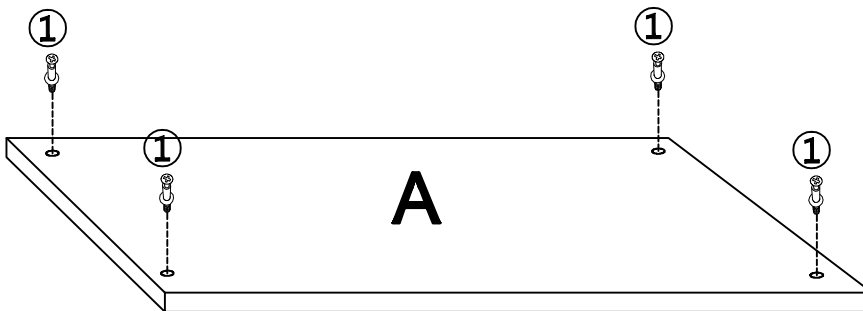
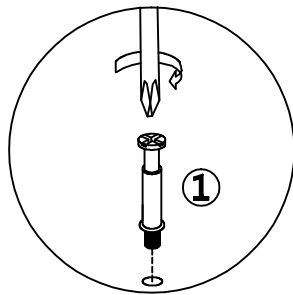
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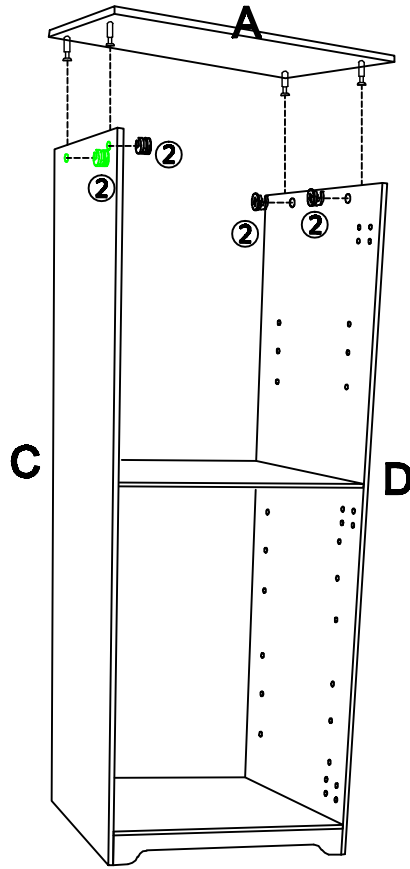
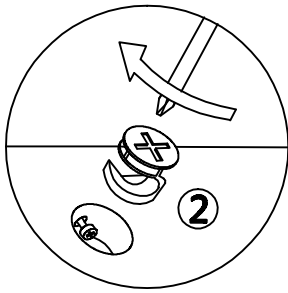
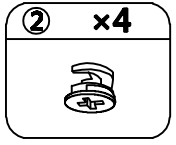
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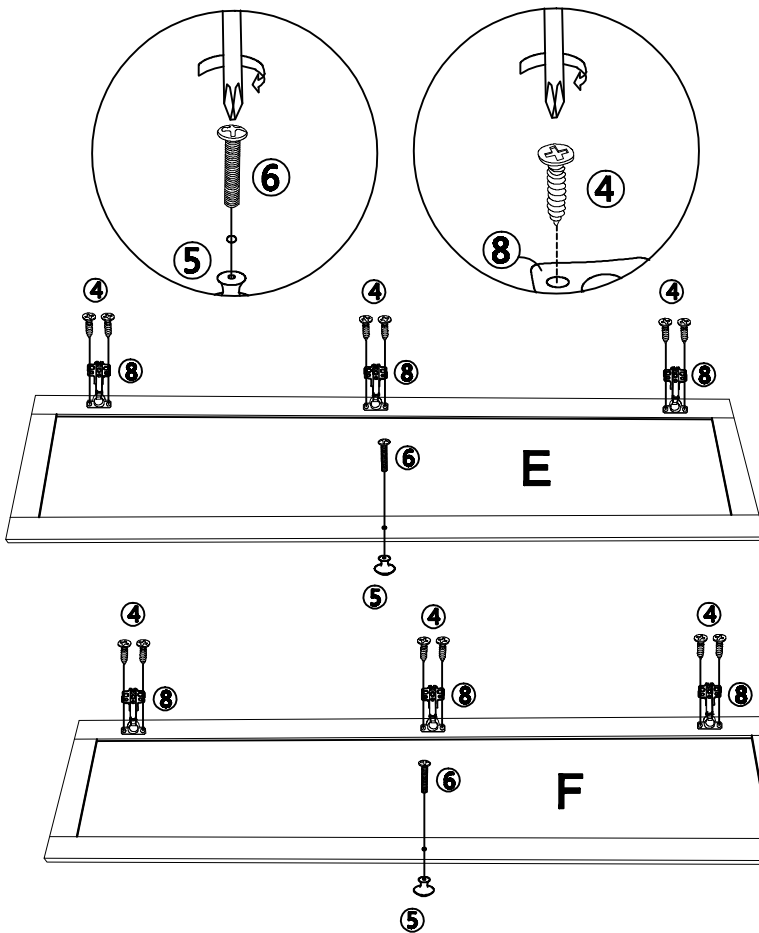
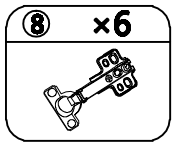
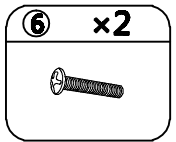
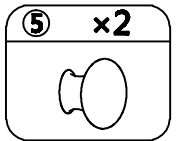
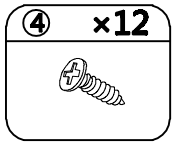
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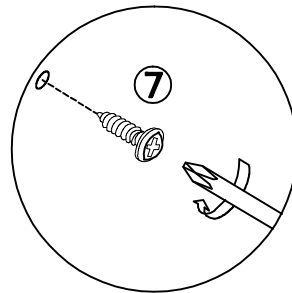
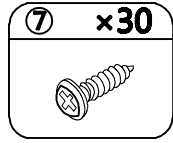
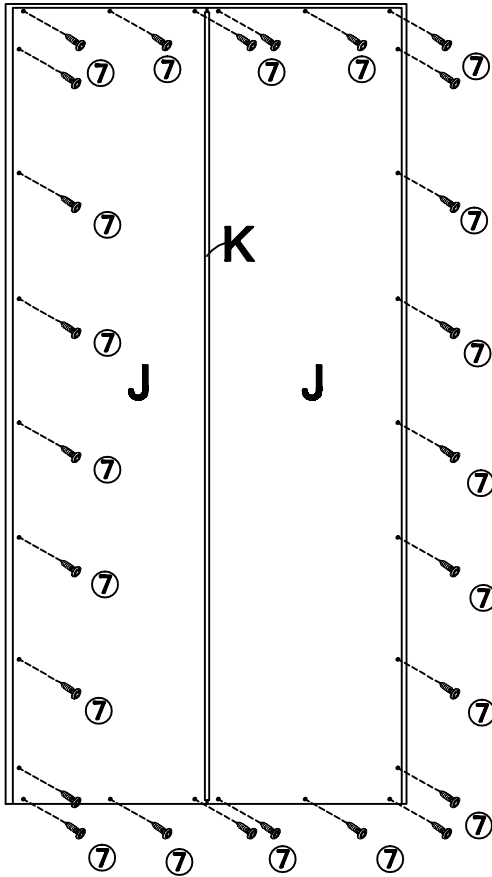
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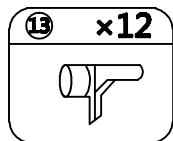
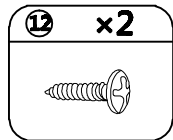
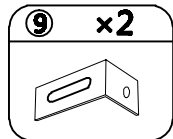
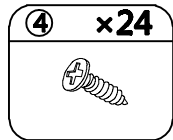
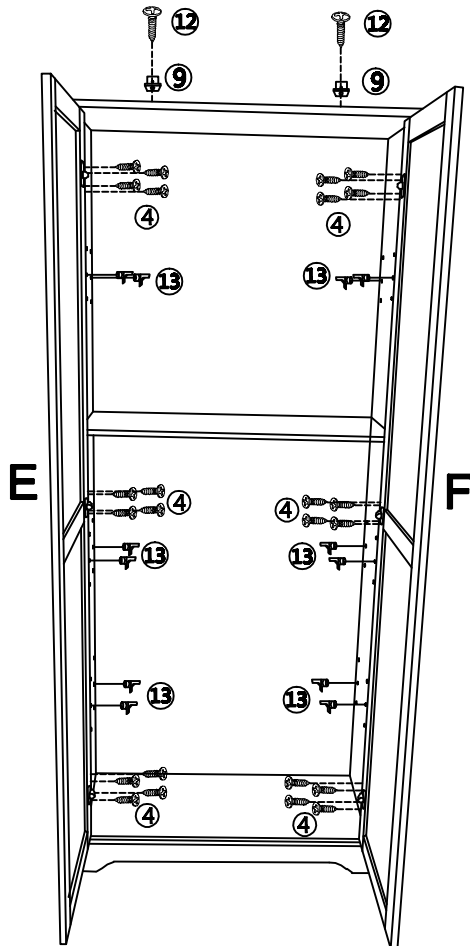
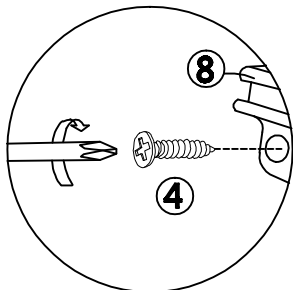
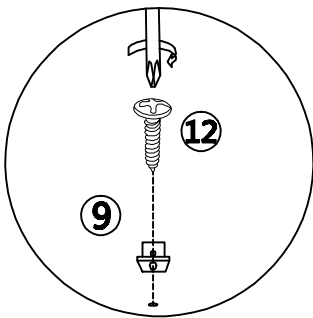
6



7



8



9

