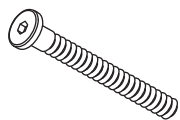


Hardware



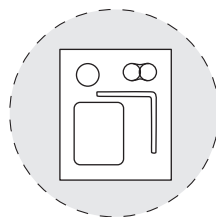
H1
8x



H2
8x

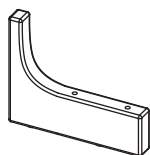


H3
8x



T1
1x

Components



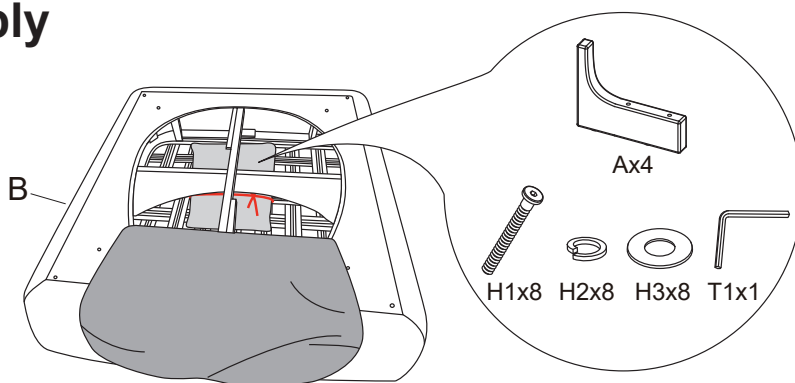
A
4x



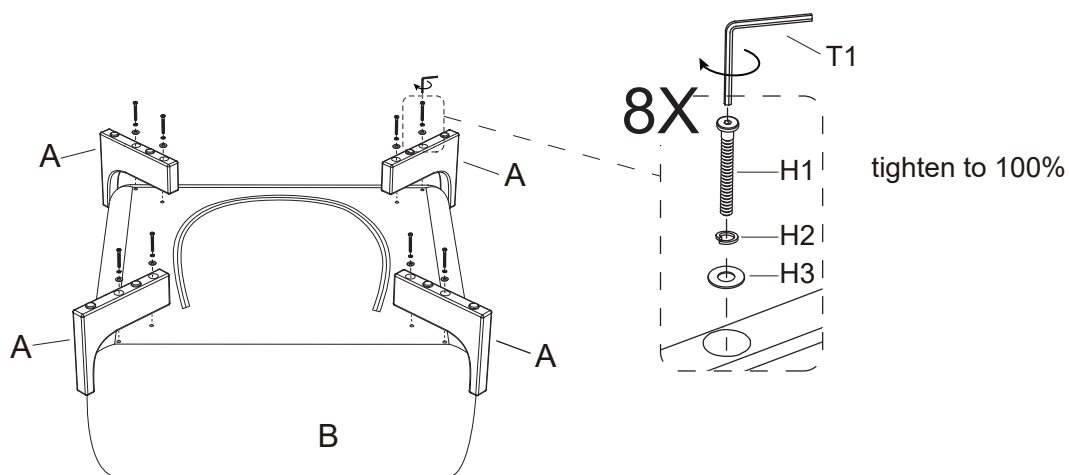
B
1x

Assembly

1



2



3

